EFR32 Mighty Gecko Family EFR32MG1 with Integrated Serial Flash Errata History

This document contains the errata history for EFR32MG1 with Integrated Serial Flash devices.

For errata on latest revision, please refer to the errata for the device. The device data sheet explains how to identify chip revision, either from package marking or electronically.

Errata history effective date: November 11th, 2016.

1. Errata Summary

Table 1.1. Errata History Overview

Designator	Title/Problem	Exists on Revision:
		C
ADC_E202	Wait After POR or EM4S Wakeup	X
ADC_E206	PROGERRIF (Program Error Interrupt Flag) Will Not Clear	X
ADC_E207	ADC Scan Repeat Mode with APORT	X
ADC_E208	ADC Interrupt Flags	X
ADC_E209	ADC and PRS Triggers	X
ADC_E210	ADC with PRS and Software Triggers	X
ADC_E211	ADC Single Repeat Mode and Tailgating	X
ADC_E212	ADC with PRS in ASYNC Mode	X
ADC_E213	ADC KEEPINSLOWACC Mode	X
ADC_E214	Using ADC CHCONMODE with PRS	X
ADC_E215	ADC CHCONMODE Set to MAXRESP Causes Extra Latency	X
ADC_E216	ADC Conversion Start Delay	X
ADC_E217	Multiple CLK Mode Switches	X
ADC_E218	SINGLEACT and SCANACT Status Flags Delayed	X
ADC_E219	STOP Command Causing FIFO Corruption	X
ADC_E220	AUXHFRCO in ASYNC mode with ASYNC CLK in ASNEEDED mode	X
ADC_E221	ADC Temperature Sensor Must be Used in LOWACC Mode	X
ADC_E222	ADC EM2 Wakeup on a Comparator Match Disables EM2 Entry	X
ADC_E223	Delayed ADC Conversion or Warmup Start	X
CORE_E201	SYSTICK and an External Clock	X
DBG_E201	AUXHFRCO Debug Limitations	X
DBG_E202	Debug Access to ADC and LEUART not Functioning as Intended	X
DCDC_E202	Regulated DCDC Output Can Dip on EM2 Entry	X
DCDC_E203	Regulated DCDC Output Can Dip on EM2 Entry if not in LN Mode	X
EFR_E201	Bit Access Not Supported for Low Energy Peripherals	X
EFR_E202	Read-Clear Access for LETIMER0 and RTCC Interrupts	X
EMU_E201	High Temperature Operation	X
EMU_E204	Restrictions Writing TEMPHIGH and TEMPLOW	X
EMU_E205	Restrictions Reading TEMP	X
EMU_E207	GPIO State can be Lost During EM4 Recovery	X
EMU_E208	Occasional Full Reset After Exiting EM4H	X
EMU_E209	Potential EM2 Lock-up when using IDAC or the Debugger with the LDMA	X
EMU_E210	Potential Power-Down When Entering EM2	X

Designator	Title/Problem	Exists on Revision:
		C
FLASH_E201	Potential Program Failure after Power On	X
I2C_E201	I2C ABORT Command	X
IDAC_E201	IDAC CURSTABLE Bit Not Reliable	X
LEUART_E201	Restrictions Setting TXDMAWU/RXDMAWU of LEUARTn_CTRL	X
RADIO_E202	802.15.4 Channel 14	X
RADIO_E203	Bluetooth Smart Channel 26	X
RADIO_E204	Increased EVM on Selected Channels	X
RMU_E201	CTRL Register Reset on All Resets	X
RTCC_E201	RTCC Does Not Support Compare/Capture Wrap with Prescaler	X
RTCC_E202	RTCC Triggers to LETIMER Not Safe	X
TIMER_E201	Timer in Input Capture Mode Can Stop Counting	X

Table 1.2. Errata Status Summary

Errata #	Designator	Title/Problem	Workaround Exists	Affected Revision	Resolution
1	RADIO_E202	802.15.4 Channel 14	No	C	Documented in the revision 1.0 data sheet
2	RADIO_E203	Bluetooth Smart Channel 26	No	C	Documented in the revision 1.0 data sheet
3	RADIO_E204	Increased EVM on Selected Channels	No	C	Documented in the revision 1.0 data sheet

2. Detailed Errata Descriptions

2.1 RADIO_E202 – 802.15.4 Channel 14

Description of Errata
Receive sensitivity of 802.15.4 channel 14 is lower than expected. The average receive sensitivity of 802.15.4 channel 14 is currently –98 dBm.
Affected Conditions / Impacts
The average receive sensitivity of 802.15.4 channel 14 is currently lower than expected.
Workaround
There is currently no workaround for this issue.
Resolution
This issue is documented in the revision 1.0 and later device data sheet.

2.2 RADIO_E203 – Bluetooth Smart Channel 26

Description of Errata
Receive sensitivity of Bluetooth Smart channel 26 (2458 MHz) is lower than expected. The average receive sensitivity of Bluetooth Smart Channel 26 is currently –86 dBm (Reference Signal) and –85 dBm (Dirty Transmitter).
Affected Conditions / Impacts
Receive sensitivity of Bluetooth Smart channel 26 is worse than expected.
Workaround
There is currently no workaround for this issue.
Resolution
This issue is documented in the revision 1.0 and later device data sheet.

2.3 RADIO_E204 – Increased EVM on Selected Channels

Description of Errata
EVM is increased for one 802.15.4 channel.
Affected Conditions / Impacts
Typical EVM will be increased to 7.8% for the 2415 MHz 802.15.4 channel.
Workaround
No workaround required. The 802.15.4 specification requires an EVM of less then 35%.
Resolution
This issue is documented in the revision 1.0 and later device data sheet.

3. Revision History

3.1 Revision 1.5

November 11, 2016

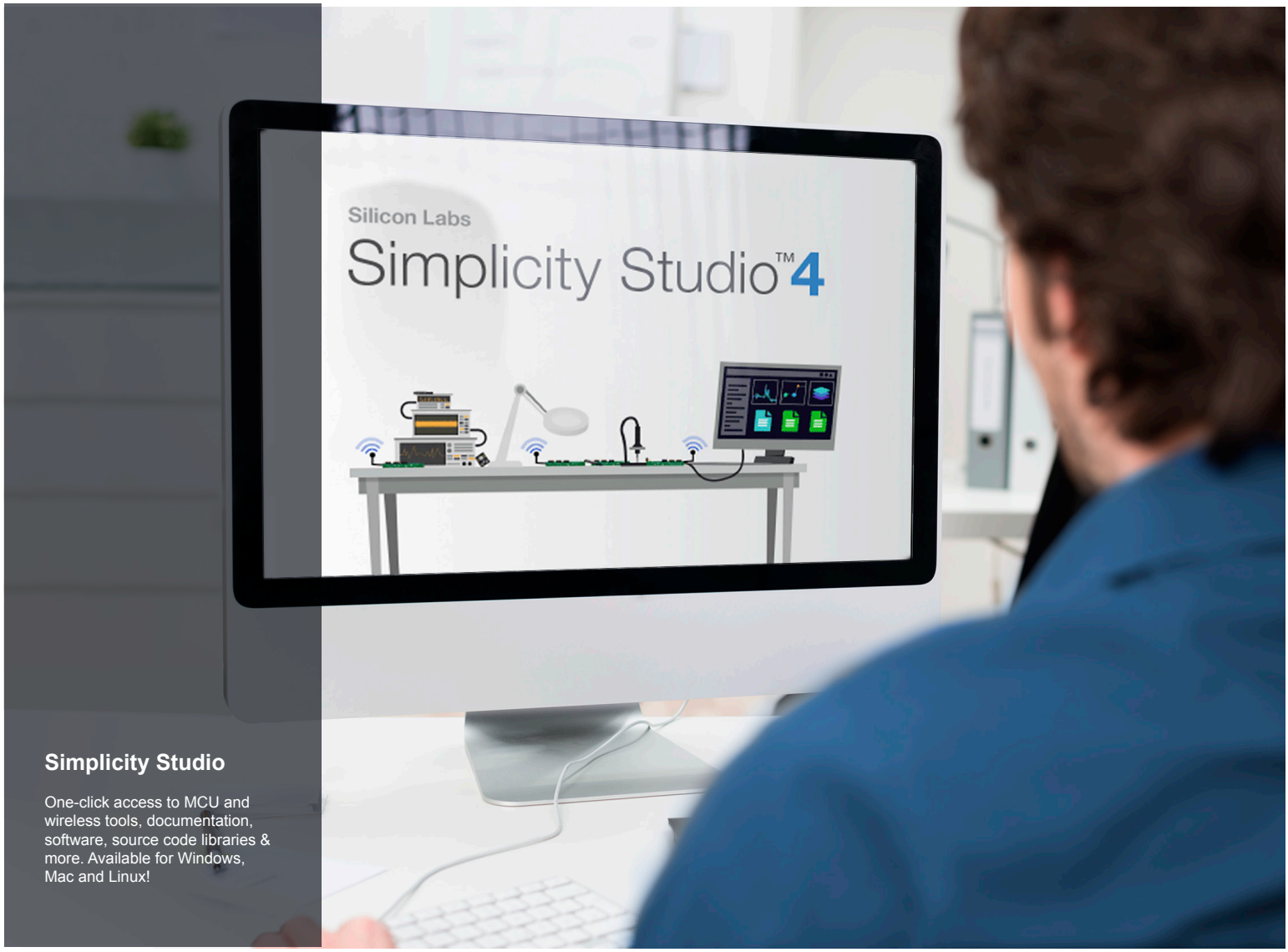
Added DCDC_E202 and DCDC_E203 to Table 1.1 Errata History Overview on page 1.

3.2 Revision 1.4

July 13th, 2016

Initial revision.

Moved RADIO_E202, RADIO_E203, and RADIO_E204 from the errata to the errata history.



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