Zen Gecko
EFR32ZG14 Errata

This document contains information on the EFR32ZG14 errata. The latest available revision of this device is revision B.

For errata on older revisions, refer to the errata history section for the device. The revision information is typically specified in or near the trace code on the device. Refer to the package marking information in the data sheet for more information.

Errata effective date: March, 2019.

1. Active Errata Summary

These tables list all known errata for the EFR32ZG14 and all unresolved errata in revision B of the EFR32ZG14.

Table 1.1. Errata History Overview

Designator	Title/Problem	Exists on Revision:
		B
DBG_E204	Debug Recovery with JTAG Does Not Work	X
RMU_E202	External Debug Access Not Available After Watchdog or Lockup Full Reset	X

Table 1.2. Active Errata Status Summary

Errata #	Designator	Title/Problem	Workaround Exists	Affected Revision	Resolution
1	DBG_E204	Debug Recovery with JTAG Does Not Work	Yes	B	—
2	RMU_E202	External Debug Access Not Available After Watchdog or Lockup Full Reset	Yes	B	—

2. Detailed Errata Descriptions

2.1 DBG_E204 – Debug Recovery with JTAG Does Not Work

Description of Errata
The debug recovery algorithm of holding down pin reset, issuing a System Bus Stall AAP instruction, and releasing the reset pin does not work when using the JTAG debug interface. When using the JTAG debug interface, the core will continue to execute code as soon as the reset pin is released.
Affected Conditions / Impacts
The debug recovery sequence will not work when using the JTAG debug interface.
Workaround
Use the Serial Wire debug interface to implement the debug recovery sequence.
Resolution
There is currently no resolution for this issue.

2.2 RMU_E202 – External Debug Access Not Available After Watchdog or Lockup Full Reset

Description of Errata
When a reset is triggered in full-reset mode, a debugger will not be able to read AHB-AP or ARM core registers.
Affected Conditions / Impacts
Systems using the full reset mode for watchdog or lockup resets will see limited debugging capability after one of these resets triggers.
Workaround
There are three possible workarounds: • Software should configure peripherals to either LIMITED or EXTENDED mode if full debugger functionality is needed after a watchdog or lockup reset.   • When using FULL reset mode, appending at least 9 idle clock cycles to the last debug command will allow the transaction to complete.   • A power cycle or hard pin reset will restore normal operation.
Resolution
There is currently no resolution for this issue.

3. Errata History

This section contains the errata history for EFR32ZG14 devices.

For errata on latest revision, refer to the beginning of this document. The device data sheet explains how to identify chip revision, either from package marking or electronically.

3.1 Errata History Summary

This table lists all resolved errata for the EFR32ZG14.

Table 3.1. Errata History Status Summary

Errata #	Designator	Title/Problem	Workaround Exists	Affected Revision	Resolution
There are no errata in the errata history for this device.					

4. Revision History

Revision 0.1

March, 2019

- Initial release.

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