



# 181001400 EFR32MG14, EFR32BG14 & EFR32FG14 Data Sheet Revision 1.2 and Errata Update

**PCN Issue Date:** 10/1/2018

**Effective Date:** 1/7/2019

**PCN Type:** Datasheet

## Description of Change

Silicon Labs is pleased to announce Data Sheet Revision 1.2 for EFR32MG14 Mighty Gecko Multi-Protocol, EFR32BG14 Blue Gecko Bluetooth® Low Energy and EFR32FG14 Flex Gecko Proprietary Wireless SoC families.

Notable changes from previous revisions are:

- Remove True Random Number Generator
- Changed “ DVDD = RFVDD = External PAVDD” in Sub-GHz specification tables to “ DVDD = RFVDD = External PA”. The PAVDD pin is for the 2.4 GHz Radio.

The Errata is updated to Revision 03/0.4 and the following new errata items have been added:

- EMU\_E214: Device Erase Cannot Occur if Voltage Scaling Level is Too Low
- I2C\_E206: Slave Holds SCL Low After Losing Arbitration

Additionally, TRNG\_E202 errata has been removed.

## Reason for Change

Remove TRNG per Silicon Errata TRNG\_E202: Standards Non-Compliance announced on May 29, 2018. Announce new revision of Datasheet and Errata.

## Impact on Form, Fit, Function, Quality, Reliability

TRNG functionality is being removed from the Datasheet. There is no change for form, fit, quality and reliability.

## Product Identification

EFR32MG14P632F256GM32-B  
EFR32MG14P632F256GM32-BR  
EFR32MG14P632F256GM48-B  
EFR32MG14P632F256GM48-BR  
EFR32MG14P733F256GM48-B  
EFR32MG14P733F256GM48-BR  
EFR32MG14P733F256IM48-B  
EFR32MG14P733F256IM48-BR  
EFR32MG14P732F256GM32-B  
EFR32MG14P732F256GM32-BR  
EFR32MG14P732F256GM48-B  
EFR32MG14P732F256GM48-BR  
EFR32MG14P732F256IM32-B  
EFR32MG14P732F256IM32-BR  
EFR32MG14P732F256IM48-B  
EFR32MG14P732F256IM48-BR  
EFR32BG14P532F256GM32-B  
EFR32BG14P532F256GM32-BR  
EFR32BG14P532F256GM48-B  
EFR32BG14P532F256GM48-BR

EFR32BG14P632F256GM32-B  
EFR32BG14P632F256GM32-BR  
EFR32BG14P632F256GM48-B  
EFR32BG14P632F256GM48-BR  
EFR32BG14P632F256IM32-B  
EFR32BG14P632F256IM32-BR  
EFR32BG14P632F256IM48-B  
EFR32BG14P632F256IM48-BR  
EFR32BG14P733F256GM48-B  
EFR32BG14P733F256GM48-BR  
EFR32BG14P732F256GM32-B  
EFR32BG14P732F256GM32-BR  
EFR32BG14P732F256GM48-B  
EFR32BG14P732F256GM48-BR  
EFR32FG14P233F128GM48-B  
EFR32FG14P233F128GM48-BR  
EFR32FG14P233F256GM48-B  
EFR32FG14P233F256GM48-BR  
EFR32FG14P232F128GM32-B  
EFR32FG14P232F128GM32-BR  
EFR32FG14P232F128GM48-B  
EFR32FG14P232F128GM48-BR  
EFR32FG14P232F256GM32-B  
EFR32FG14P232F256GM32-BR  
EFR32FG14P232F256GM48-B  
EFR32FG14P232F256GM48-BR  
EFR32FG14P231F128GM32-B  
EFR32FG14P231F128GM32-BR  
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EFR32FG14P231F256GM48-B  
EFR32FG14P231F256GM48-BR  
EFR32FG14P231F256IM32-B  
EFR32FG14P231F256IM32-BR  
EFR32FG14P231F256IM48-B  
EFR32FG14P231F256IM48-BR

**Last Date of Unchanged Product:** 1/7/2019

### Qualification Samples

Available upon request.

### Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to [PCNEarlyAcceptance@silabs.com](mailto:PCNEarlyAcceptance@silabs.com)

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### Qualification Data

Available upon request.



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