



191213694 EFR32xG12 Data Sheet Revision 1.5 and Errata Revision 0.6

Bulletin Issue Date: 12/13/2019

Effective Date: 12/13/2019

Description of Change

Silicon Labs is pleased to announce version 1.5 of EFR32BG12, EFR32FG12 and EFR32MG12 data sheets. Version 1.5 corrects several errors and minor typos in the document. The errata has also been updated to version 0.6.

The key updates to the data sheet are:

- Updated the Low Energy Timer documentation to correctly reference support for EM3
- Updated absolute voltage on the 2.4 GHz and sub-GHz RF pins from 3.3 to 3.8 volts
- Added note that TRNG operation is only supported at VSCALE2

New errata have been found and work arounds documented that effect multiple peripherals, including Capacitive Sense, I2C, and USART. In addition, an issue was identified that effects Bluetooth false detection determination.

See the Revision History of the data sheet and errata documents for a complete list of changes.

Reason for Change

Data Sheet revision to correct several errors and minor typos and errata revision for new issues and updated workarounds.

Product Identification

Existing Part #
EFR32BG12P132F1024GL125-C
EFR32BG12P132F1024GL125-CR
EFR32BG12P132F1024GM48-C
EFR32BG12P132F1024GM48-CR
EFR32BG12P232F1024GL125-C
EFR32BG12P232F1024GL125-CR
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EFR32BG12P232F1024GM68-CR
EFR32BG12P232F1024IM68-C
EFR32BG12P232F1024IM68-CR
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EFR32MG12P433F1024IL125-C
EFR32MG12P433F1024IL125-CR
EFR32MG12P433F1024IM48-C
EFR32MG12P433F1024IM48-CR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

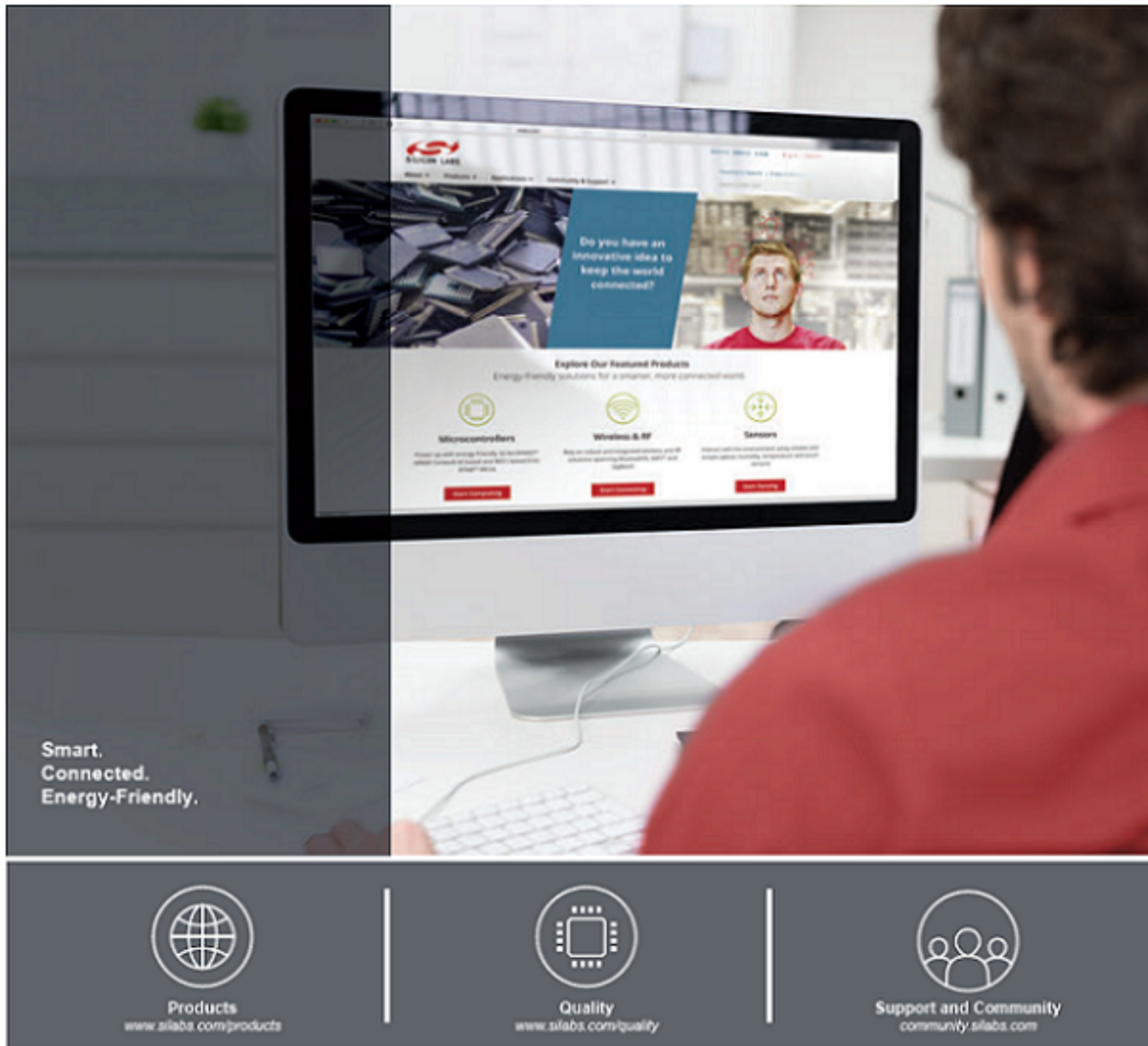
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None

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