



200505777 Errata Update BGM13S, MGM13S, MGM12P

Bulletin Issue Date: 5/5/2020

Effective Date: 5/5/2020

Description of Change

Silicon Labs is pleased to announce the release of the following errata versions for BGM13S, MGM13S and MGM12P devices. These updated errata add errata item EMU_E220 whose details can be found in the change reason and impact sections.

Release of Version 0.2 of the BGM13S Errata
Release of Version 0.2 of the MGM13S Errata
Release of Version 0.3 of the MGM12P Errata

There is no change to form, fit, quality or reliability. Function is impacted as described below:

When exiting energy mode 2 (EM2) or energy mode 3 (EM3) there will now be a 2.7us increase in the wake up time. This will be implemented in GSDK Version 2.7.4.0 and beyond by updating EMLIB. The impacted functions are EMU_EnterEM2() and EMU_EnterEM3() in em_emu.c as well as CHIP_Init() in em_chip.h.

This workaround is included in em_emu.c in the v2.7.4.0 or later of the Gecko SDK. It is recommended to workaround this issue by using the latest Gecko SDK version.

Please contact your local Silicon Labs representative for further information.

Reason for Change

Errata item EMU_E220: Infrequently, when waking from energy mode 2 (EM2) or energy mode 3 (EM3), if using voltage scaling the device will experience a decouple voltage brownout (DECBO) reset. To prevent this from happening, the wake up sequence when exiting EM2 and EM3 when using voltage scaling has been updated.

Product Identification

Existing Part #
BGM13S22F512GA-V3
BGM13S22F512GA-V3R
BGM13S22F512GN-V2
BGM13S22F512GN-V2R
BGM13S22F512GN-V3
BGM13S22F512GN-V3R
BGM13S32F512GA-V3
BGM13S32F512GA-V3R
BGM13S32F512GN-V2
BGM13S32F512GN-V2R
BGM13S32F512GN-V3
BGM13S32F512GN-V3R
MGM12P02F1024GA-V2
MGM12P02F1024GA-V2R
MGM12P02F1024GA-V4
MGM12P02F1024GA-V4R
MGM12P02F1024GE-V2
MGM12P02F1024GE-V2R
MGM12P02F1024GE-V4
MGM12P02F1024GE-V4R
MGM12P22F1024GA-V2
MGM12P22F1024GA-V2R
MGM12P22F1024GA-V4
MGM12P22F1024GA-V4R
MGM12P22F1024GE-V4

MGM12P22F1024GE-V4R
MGM12P22XXXXXGA-V4
MGM12P22XXXXXGA-V4R
MGM12P32F1024GA-V2
MGM12P32F1024GA-V2R
MGM12P32F1024GA-V4
MGM12P32F1024GA-V4R
MGM12P32F1024GE-V2
MGM12P32F1024GE-V2R
MGM12P32F1024GE-V4
MGM12P32F1024GE-V4R
MGM13S02F512GA-V2
MGM13S02F512GA-V2R
MGM13S02F512GA-V3
MGM13S02F512GA-V3R
MGM13S02F512GN-V2
MGM13S02F512GN-V2R
MGM13S02F512GN-V3
MGM13S02F512GN-V3R
MGM13S12F512GA-V2
MGM13S12F512GA-V2R
MGM13S12F512GA-V3
MGM13S12F512GA-V3R
MGM13S12F512GN-V2
MGM13S12F512GN-V2R
MGM13S12F512GN-V3
MGM13S12F512GN-V3R

Where 'X' denotes a custom part number.

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

Customer Actions Needed:

None

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>