



201013898 EFR32MG/EFR32FG14 Datasheet Release v1.3

Bulletin Issue Date: 10/13/2020

Effective Date: 10/13/2020

Description of Change

Silicon Labs is pleased to announce a new datasheet revision 1.3 for EFR32MG14/EFR32FG14 devices.

Reason for Change

Revision 1.3

- In 1. Feature List, removed Wake on Radio reference.
- In the front page block diagram, updated the lowest energy mode for LETIMER.
- Updated 3.6.4 Low Energy Timer (LETIMER) lowest energy mode.
- In Table 4.2 General Operating Conditions on page 23, Added additional test condition rows (VSCALE2/WS0, VSCALE0/WS1/WS0) to HFCORECLK frequency specification.
- In, Table 4.38 General-Purpose I/O (GPIO) on page 80, added footnotes to VIL, VIH (footnote 1), and RPUD (footnote 2).
- In Table 4.39 Voltage Monitor (VMON) on page 82, updated test conditions for IVMON.
- In Table 4.42 Digital to Analog Converter (VDAC) on page 88, updated test conditions for IDAC, 200 Hz refresh rate.
- Updated External PA supply connection conditions for SPURHARM_ETSI and SPUROOB_ETSI in section 4.1.10.3 Sub-GHz RF Transmitter characteristics for 868 MHz Band.
- In Table 4.18 Sub-GHz RF Transmitter characteristics for 915 MHz Band on page 50:
 - Corrected FCC reference for non-restricted bands in:
 - SPURHARM_FCC_20
 - SPUROOB_FCC_20
 - SPURHARM_FCC_14
 - SPUROOB_FCC_14
 - Updated typical and maximum specifications for restricted bands (30-88 MHz) in:
 - SPUROOB_FCC_20
 - SPUROOB_FCC_14
 - Added footnote to PSD.
- In 4.1.10.2 Sub-GHz RF Receiver Characteristics for 915 MHz Band, updated typical and maximum specifications for SPURRX_ARIB, 930-1000 MHz, RBW=100 kHz.
- In 7.3 QFN48 Package Marking, updated feature code.
- In 8.3 QFN32 Package Marking, updated feature code.
- Minor typographical, style, and phrasing changes throughout document.

Product Identification

Existing Part #
EFR32MG14P632F256GM32-B
EFR32MG14P632F256GM32-BR
EFR32MG14P632F256GM48-B
EFR32MG14P632F256GM48-BR
EFR32MG14P732F256GM32-B
EFR32MG14P732F256GM32-BR
EFR32MG14P732F256GM48-B
EFR32MG14P732F256GM48-BR
EFR32MG14P732F256IM32-B
EFR32MG14P732F256IM32-BR
EFR32MG14P732F256IM48-B
EFR32MG14P732F256IM48-BR
EFR32MG14P733F256GM48-B
EFR32MG14P733F256GM48-BR
EFR32MG14P733F256IM48-B

EFR32MG14P733F256IM48-BR
EFR32FG14P231F128GM32-B
EFR32FG14P231F128GM32-BR
EFR32FG14P231F128GM48-B
EFR32FG14P231F128GM48-BR
EFR32FG14P231F256GM32-B
EFR32FG14P231F256GM32-BR
EFR32FG14P231F256GM48-B
EFR32FG14P231F256GM48-BR
EFR32FG14P231F256IM32-B
EFR32FG14P231F256IM32-BR
EFR32FG14P231F256IM48-B
EFR32FG14P231F256IM48-BR
EFR32FG14P232F128GM32-B
EFR32FG14P232F128GM32-BR
EFR32FG14P232F128GM48-B
EFR32FG14P232F128GM48-BR
EFR32FG14P232F256GM32-B
EFR32FG14P232F256GM32-BR
EFR32FG14P232F256GM48-B
EFR32FG14P232F256GM48-BR
EFR32FG14P233F128GM48-B
EFR32FG14P233F128GM48-BR
EFR32FG14P233F256GM48-B
EFR32FG14P233F256GM48-BR
EFR32FG14V132F256GM32-B
EFR32FG14V132F256GM32-BR
EFR32FG14V132F256GM48-B
EFR32FG14V132F256GM48-BR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

Customer Actions Needed:

None

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>