



201020904 EFM32JG12/EFM32PG12 Datasheet Release v1.2

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Description of Change

Silicon Labs is pleased to announce the release of version 1.2 of the EFM32JG12 and EFM32PG12 datasheet. This datasheet update is considered minor and is to correct errors, typos, formatting, and to add notes to certain peripherals.

Release of Version 1.2 of the EFM32JG12 Datasheet
Release of Version 1.2 of the EFM32PG12 Datasheet

Reason for Change

Revision 1.2

- In the front page block diagram, updated the lowest energy mode for LETIMER.
- Updated 3.5.4 Low Energy Timer (LETIMER) lowest energy mode.
- Added a Note about the operating voltage in 3.7.3 True Random Number Generator (TRNG).
- Reworded or removed mentions of “modules” in reference to device peripherals in system overview.

Product Identification

Existing Part #
EFM32JG12B500F1024GL125-C
EFM32JG12B500F1024GL125-CR
EFM32JG12B500F1024GM48-C
EFM32JG12B500F1024GM48-CR
EFM32JG12B500F1024IL125-C
EFM32JG12B500F1024IL125-CR
EFM32JG12B500F1024IM48-C
EFM32JG12B500F1024IM48-CR
EFM32PG12B500F1024GL125-C
EFM32PG12B500F1024GL125-CR
EFM32PG12B500F1024GM48-C
EFM32PG12B500F1024GM48-CR
EFM32PG12B500F1024IL125-C
EFM32PG12B500F1024IL125-CR
EFM32PG12B500F1024IM48-C
EFM32PG12B500F1024IM48-CR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

None

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