201104924 EFP01 Data Sheet Rev 1.1

Bulletin Issue Date: 11/4/2020 **Effective Date:** 11/4/2020

Description of Change

Silicon Labs is pleased to announce availability of Data Sheet Rev 1.1 for the EFP01 Energy Friendly PMIC.

Reason for Change

Revision 1.1
Oct, 2020

• Added EFP0110 OPN to the following sections:
- 1. Feature List
- Table 3.1 OPN-Specific Features on page 8
- Table 2.1 Ordering Information on page 3
- 5.1.3.6 EFP0110
- Table 3.2 DCDC A Configuration and Modes Summary on page 9
- 3.2.4 Coarse Regulators
- 7.2 OTP Defaults
• In EFP0104 VOA/VOC Electrical Specs:
- Corrected Output programmable voltage range maximum value from 5.2V to 3.3V.
- Corrected Output voltage accuracy conditions from 1.8V <= VOA <= 5.0V to 1.8V <= VOA <= 3.3V
• Removed Wired Buck Configuration from 4.1 Device Configurations and Table 3.2 DCDC A Configuration and Modes Summary on page 9
• Corrected all OPNs in Table 3.2 DCDC A Configuration and Modes Summary on page 9 and 2. Ordering Information
• Corrected valid OTP_TEMP_CODE and OTP_OPN_NUM enumerations in 7.1 OTP Definition
• Corrected EFP0109 Input Voltage Range in Table 2.1 Ordering Information on page 3
• In Figure 4.5 Boost Bootstrap Configuration on page 33, corrected VOB output capacitor from 2.2uF to 10uF, added note about VOA capacitor value, added notes on VOC Output capacitors
• In 4.1.4 Wired Boost Configuration (e.g., EFP0109, EFP0110), added clarification of VOB output capacitor size and added notes on VOC Output capacitors
• In 4.1.3 Single-Cell Boost Configuration (e.g., EFP0108) and 4.1.2 Buck/Boost Configuration , added notes on VOC Output capacitors
• Added VOB=1.8V Output Voltage Accuracy Specs for the EFP0109 VOB output.
• In all VOB and VOC Electrical Specification tables, clarified footnote indicating that when VDDB was powered by VOA (EFP0108and EFP0111), the DCDC B and LDO C output current is also limited by the VOA output current.
• Added disclaimer clarifying that Maximum Output Current formulas are not a guarantee of performance to the DCDC A Peak Current Configuration, DCDC B Peak Current Configuration, and LDO Maximum Output Current sections
• In 5.1.3.7 EFP0111 Electrical Specifications, corrected Maximum output current footnote test current (1.25mA instead of 5mA) and added details on how to interpret this specification for Boost Bootstrap.
• Added note to 3.3.4 EM4 section that dedicated EM4 hardware support was available for EFP01 starting on EFR32xG22 devices.
• Clarified recommended reset mechanisms and hazards in 3.5.1 OTP section and 6.2.11 CMD register description

Product Identification

```
Existing Part #
EFP0104GM20-D
EFP0104GM20-DR
EFP0108GM20-D
EFP0108GM20-DR
EFP0109GM20-D
EFP0109GM20-DR
EFP0110GM20-D
EFP0110GM20-DR
EFP0111GM20-D
EFP0111GM20-DR
```

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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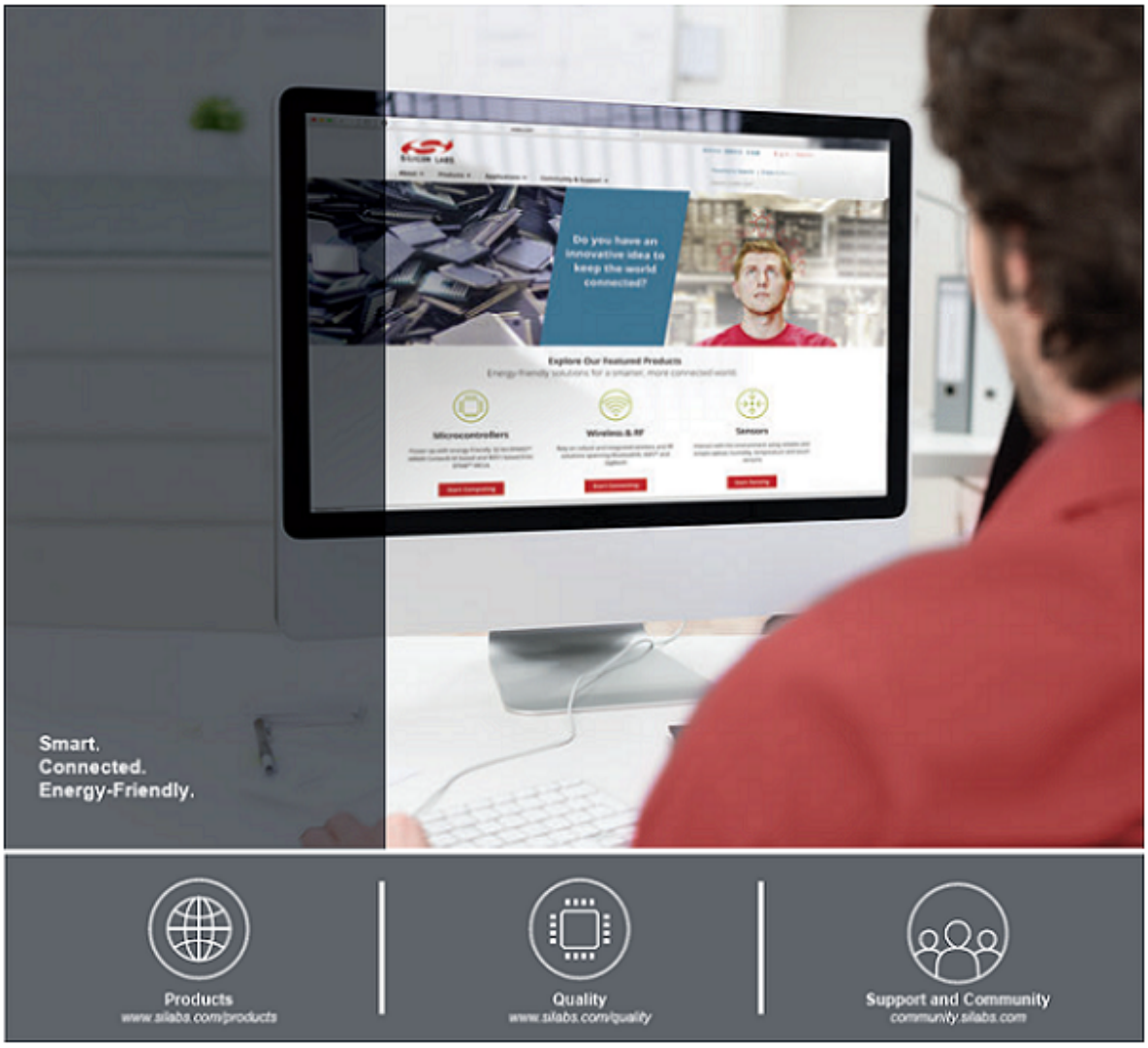

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