



2202221191 CP2110 v1.3 Datasheet Release

Bulletin Issue Date: Feb 22, 2022

Effective Date: Feb 22, 2022

Description of Change

Silicon Labs is pleased to announce the release of CP2110 v1.3 datasheet version.

Reason for Change

Revision 1.2 to Revision 1.3

- Updated VIO requirement in Note 1 of Figure 11, "Typical Bus-Powered Connection Diagram," on page 22 and in Note 1 of Figure 12, "Typical Self-Powered Connection Diagram (Regulator Bypass)," on page 23.

- Changed VDD to VIO in Note 4 of Figure 11, "Typical Bus-Powered Connection Diagram," on page 22, in Note 4 of Figure 12, "Typical Self-Powered Connection Diagram (Regulator Bypass)," on page 23, and in "9. One-Time Programmable ROM" on page 21.

- Updated the information about serialization of CP210x in "9. One-Time Programmable ROM" on page 21.
- Updated Application Note link on "12. Relevant Application Notes" on page 24.
- Updated the title of AN721 throughout.

Product Identification

Existing Part #
CP2110-F01-GM
CP2110-F01-GMR
CP2110-F02-GM1
CP2110-F02-GM1R

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

None.

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