



2206211275 EFR32ZG13, ZG13L, ZG13S Datasheet Release v1.3

PCN Issue Date: Jun 21, 2022

Effective Date: Sep 27, 2022

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.3 for EFR32ZG13, EFR32ZG13L and EFR32ZG13S devices.

Notable changes from previous revisions are:

- Updated 3.6.3 Real Time Counter and Calendar (RTCC) to remove AUXHFRCO as a clock source.
- Added timing specifications for RESETn low time and clarified VIL and VIH logic levels for RESETn pins in Table 4.25 General-Purpose I/O (GPIO) on page 52.
- Removed BIASPROG = 1, FULLBIAS = 0 specifications from Table 4.28 Analog Comparator (ACMP) on page 57.
- Added a note to Table 7.2 QFN48 PCB Land Pattern Dimensions on page 140 and Table 8.2 RQFN32 PCB Land Pattern Dimensions on page 145. (EFR32ZG13 only)
- Removed all references to RFSENSE.
- Removed references to BOOT_TX and BOOT_RX.

Reason for Change

Improve our production capability

Impact on Form, Fit, Function, Quality, Reliability

There is no change in form, fit, quality and reliability. Functionality is impacted as listed in change description.

Product Identification

Existing Part #
EFR32ZG13L231F512IM32-D
EFR32ZG13L231F512IM32-DR
EFR32ZG13P531F512GM32-D
EFR32ZG13P531F512GM32-DR
EFR32ZG13P531F512GM48-D
EFR32ZG13P531F512GM48-DR
EFR32ZG13S231F512GM32-D
EFR32ZG13S231F512GM32-DR

Last Date of Unchanged Product: Sep 27, 2022

Qualification Samples

NA

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

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Qualification Data

NA



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