



2208101322 EFR32BG12 Datasheet v1.7; EFR32FG12/MG12 Datasheet v1.8

PCN Issue Date: Aug 10, 2022

Effective Date: Nov 16, 2022

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce Data Sheet version 1.7 for EFR32BG12 & 1.8 for EFR32FG12/MG12 families.

Notable changes from previous revisions are:

- Updated to remove all references to RFSENSE and remove AUXHFRCO as a clock source.
- Removed BIASPROG = 1, FULLBIAS = 0 specifications and updated Hysteresis max values in Analog Comparator (ACMP) Table.
- Added HFSRCCLK frequency - General Operating Conditions.
- Added timing specifications for RESETn low time and clarified VIL and VIH logic levels for RESETn pins - General-Purpose I/O (GPIO).
- Added SPI Master Timing Diagram (SMSDELAY = 1).
- Added a note on BGA125; QFN48 and QFN68 PCB Land Pattern Dimensions.
- In RF Matching Networks, added document reference to IPD solutions.

Please refer to the datasheet revision history for a complete list of changes.

Link to datasheets:

<https://www.silabs.com/documents/public/data-sheets/efr32bg12-datasheet.pdf>

<https://www.silabs.com/documents/public/data-sheets/efr32fg12-datasheet.pdf>

<https://www.silabs.com/documents/public/data-sheets/efr32mg12-datasheet.pdf>

Reason for Change

Improve our production capability

Impact on Form, Fit, Function, Quality, Reliability

There is no change in form, fit, quality and reliability. Functionality is changed as described in description section.

Product Identification

This notification includes both standard and customer-specific part numbers. An asterisk * represents a number or letter (one or more) in a customer-specific part number.

Existing Part #

EFR32BG12P132F1024GL125-C
EFR32BG12P132F1024GL125-CR
EFR32BG12P132F1024GM48-C
EFR32BG12P132F1024GM48-CR
EFR32BG12P232F1024GL125-C
EFR32BG12P232F1024GL125-CR
EFR32BG12P232F1024GM48-C
EFR32BG12P232F1024GM48-CR
EFR32BG12P232F1024GM68-C
EFR32BG12P232F1024GM68-CR
EFR32BG12P232F1024IM68-C
EFR32BG12P232F1024IM68-CR
EFR32BG12P232F512GM68-C
EFR32BG12P232F512GM68-CR
EFR32BG12P232F512IM68-C
EFR32BG12P232F512IM68-CR
EFR32BG12P332F1024GL125-C

EFR32BG12P332F1024GL125-CR
EFR32BG12P332F1024GM48-C
EFR32BG12P332F1024GM48-CR
EFR32BG12P332F1024IM48-C
EFR32BG12P332F1024IM48-CR
EFR32BG12P432F1024GL125-C
EFR32BG12P432F1024GL125-CR
EFR32BG12P432F1024GM48-C
EFR32BG12P432P****GM48-C
EFR32BG12P432P****GM48-CR
EFR32BG12P433F1024GL125-C
EFR32BG12P433F1024GL125-CR
EFR32BG12P433F1024GM48-C
EFR32BG12P433F1024GM48-CR
EFR32BG12P433F1024GM68-C
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EFR32BG12P433X****GM68-C
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EFR32MG12P432F1024GL125-CR
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EFR32MG12P433F1024GM48-CR
EFR32MG12P433F1024GM68-C
EFR32MG12P433F1024GM68-CR
EFR32MG12P433F1024IL125-C
EFR32MG12P433F1024IL125-CR
EFR32MG12P433F1024IM48-C
EFR32MG12P433F1024IM48-CR

Last Date of Unchanged Product: Nov 16, 2022

Qualification Samples

NA

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

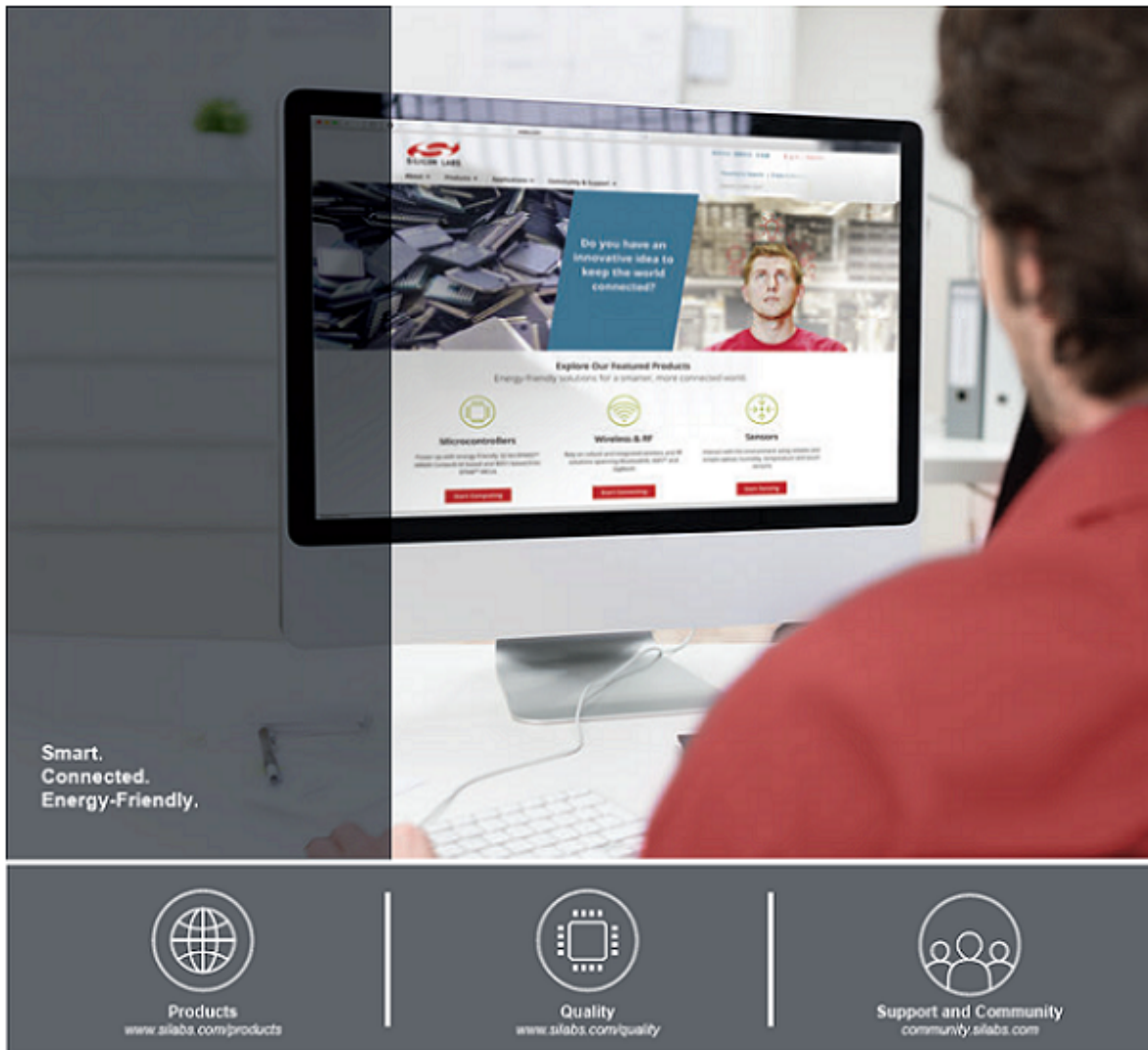
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Qualification Data

NA



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