

# 2208101323 EFM32JG12/PG12 Datasheet v1.3

PCN Issue Date: Aug 10, 2022

Effective Date: Nov 16, 2022

PCN Type: Datasheet

#### **Description of Change**

Silicon Labs is pleased to announce Data Sheet version 1.3 for EFM32JG12/PG12 families.

Notable changes from previous revisions are:

• Updated Real Time Counter and Calendar (RTCC) to remove AUXHFRCO as a clock source.

• Removed BIASPROG = 1, FULLBIAS = 0 specifications from Analog Comparator (ACMP) Table.

Added HFSRCCLK frequency - General Operating Conditions.

• Added timing specifications for RESETn low time and clarified VIL and VIH logic levels for RESETn pins - General-Purpose I/O (GPIO).

- Added SPI Master Timing Diagram (SMSDELAY = 1).
- Added a note on BGA125 and QFN48 PCB Land Pattern Dimensions.
- Updated Hysteresis max values Analog Comparator (ACMP).

• Updated note - Operational Amplifier (OPAMP) specification table.

Please refer to the datasheet revision history for a complete list of changes. Link to datasheets:

https://www.silabs.com/documents/public/data-sheets/efm32jg12-datasheet.pdf

https://www.silabs.com/documents/public/data-sheets/efm32pg12-datasheet.pdf

## **Reason for Change**

Improve our production capability

#### Impact on Form, Fit, Function, Quality, Reliability

There is no change in form, fit, quality and reliability. Functionality is changed as described in description section.

#### **Product Identification**

```
Existing Part #
EFM32JG12B500F1024GL125-C
EFM32JG12B500F1024GL125-CR
EFM32JG12B500F1024GM48-C
EFM32JG12B500F1024GM48-CR
EFM32JG12B500F1024IL125-C
EFM32JG12B500F1024IL125-CR
EFM32JG12B500F1024IM48-C
EFM32JG12B500F1024IM48-CR
EFM32PG12B500F1024GL125-C
EFM32PG12B500F1024GL125-CR
EFM32PG12B500F1024GM48-C
EFM32PG12B500F1024GM48-CR
EFM32PG12B500F1024IL125-C
EFM32PG12B500F1024IL125-CR
EFM32PG12B500F1024IM48-C
EFM32PG12B500F1024IM48-CR
```

Last Date of Unchanged Product: Nov 16, 2022

#### **Qualification Samples**

#### NA

#### **Customer Response**

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <a href="http://www.silabs.com">http://www.silabs.com</a>.

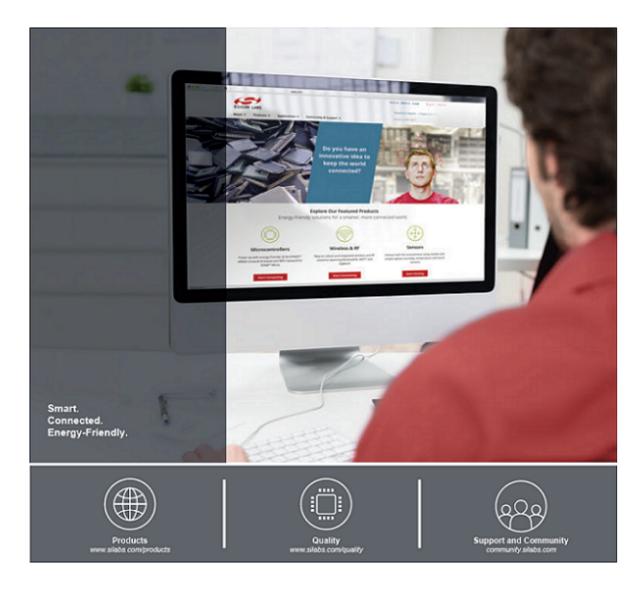
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

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### **Qualification Data**

NA



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