

2301181397 Errata Update v0.3 for BGM220P and BGM220S, v0.2 for MGM220P

Bulletin Issue Date: Jan 18, 2023 Effective Date: Jan 18, 2023

Description of Change

Silicon Labs is pleased to announce the release of errata version 0.3 for BGM220P and BGM220S as well as 0.2 for MGM220P modules.

Reason for Change

The following errata and workarounds have been added or updated:

- Added CUR E302 (Extra EM1 Current if FPU is Disabled)
- Added DCDC_E302 (DCDC Interrupts Block EM2/3 Entry or Cause Unexpected Wake-up)
- Added EUART_E301 (EUART receiver enters lockup state when using low frequency IrDA mode)
- Added EUART_E302 (Incorrect stop bits lock rx)
- Added IADC_E306 (Changing Gain During a Scan Sequence Causes an Erroneous IADC Result)
- (only BGM220P/S) Added USART_E304 (PRS Transmit Unavailable in Synchronous Secondary Mode)
- (only MGM220P) Updated the workaround in I2C_E303 (I2C Fails to Indicate New Incoming Data)

Links to errata:

BGM220P: https://www.silabs.com/documents/public/errata/bgm220p-errata.pdf BGM220S: https://www.silabs.com/documents/public/errata/bgm220s-errata.pdf MGM220P: https://www.silabs.com/documents/public/errata/mgm220p-errata.pdf

Product Identification

This notification includes both standard and customer-specific part numbers. An asterisk * represents a number or letter (one or more) in a customer-specific part number.

BGM220PC22HNA2
BGM220PC22HNA2R
BGM220PC22WGA2
BGM220PC22WGA2R
BGM220SC12WGA2
BGM220SC12WGA2R
BGM220SC22HNA2
BGM220SC22HNA2R
BGM220SC22HNA2R
BGM220SC22WGA2
BGM220SC22WGA2
BGM22P2WGPZ*A2
BM22P2WGPZ*A2R
BM22S1WPZ*A2
BM22S1WPZ*A2
BM22S1WPZ*A2

Existing Part #

BM22S2HXZ*A2R BM22S2WPZ*A2

BM22S2WPZ*A2R

MGM220PC22HNA2 MGM220PC22HNA2R

Kit Identification

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

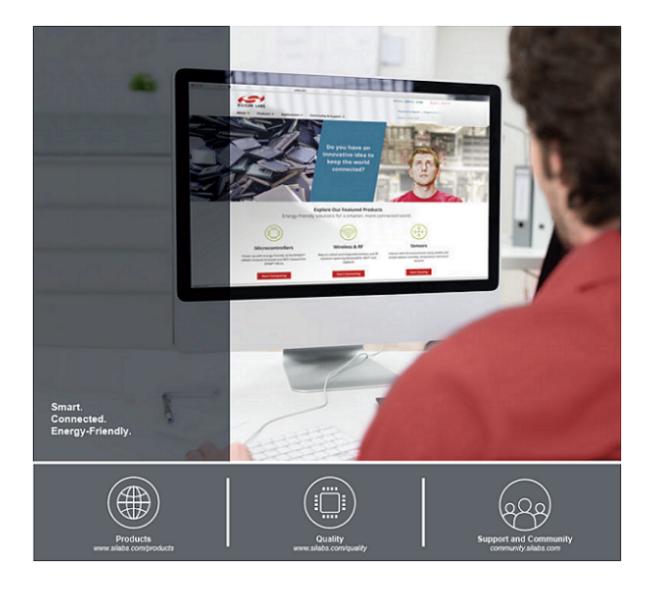
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Customer Actions Needed:

Review updated errata.

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