



2301181401 Errata Update v0.2 for FGM230S, v0.3 for ZGM230S

Bulletin Issue Date: Jan 18, 2023

Effective Date: Jan 18, 2023

Description of Change

Silicon Labs is pleased to announce the release of errata versions 0.2 for FGM230S and v0.3 for ZGM230S modules.

Reason for Change

The following errata and workarounds have been added or updated:

- EUSART_E303 (EUSART receiver enters lockup state when using low frequency IrDA mode)
- EUSART_E304 (Incorrect stop bits lock rx)
- Fixed workaround routing for KEYSCAN_E301 (Unused Rows Are Not Properly Gated Off)
- (only ZGM230S) added KEYSCAN_E301 (Unused Rows Are Not Properly Gated Off)
- (only ZGM230S) added IADC_E306 (Changing Gain During a Scan Sequence Causes an Erroneous IADC Result)
- (only ZGM230S) resolved CUR_E303 – Active Charge Pump Clock Causes High Current

Links to errata:

FGM230S: <https://www.silabs.com/documents/public/errata/fgm230s-errata.pdf>

ZGM230S: <https://www.silabs.com/documents/public/errata/zgm230s-errata.pdf>

Product Identification

Existing Part #
FGM230SA27HGN3
FGM230SA27HGN3R
FGM230SB27HGN3
FGM230SB27HGN3R
ZGM230SA27HGN2
ZGM230SA27HGN2R
ZGM230SA27HGN3
ZGM230SA27HGN3R
ZGM230SB27HGN2
ZGM230SB27HGN2R
ZGM230SB27HGN3
ZGM230SB27HGN3R

Kit Identification

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

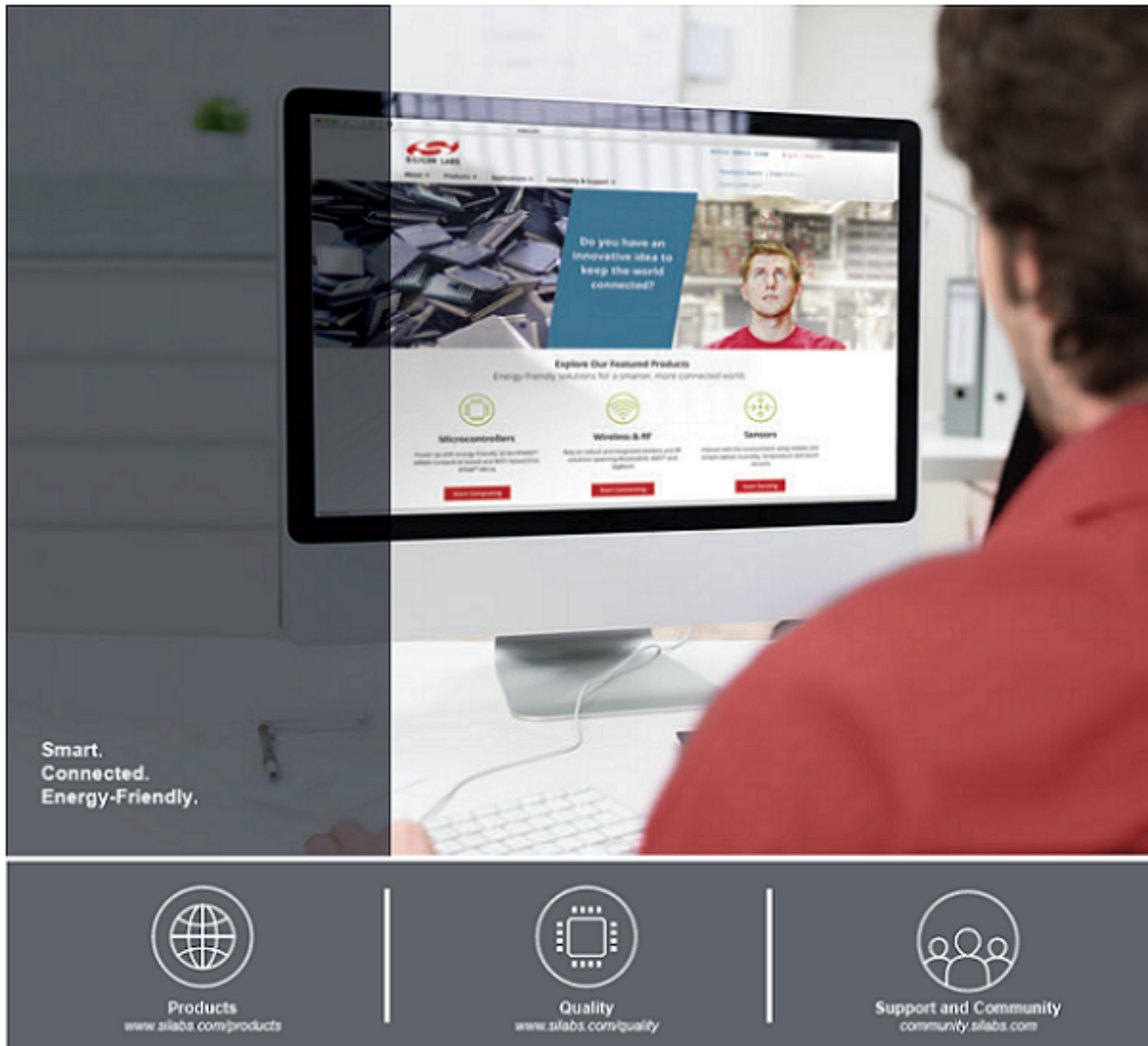
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Customer Actions Needed:

Review updated errata.

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