

2406261560 Datasheet Release v1.4 for xGM210P and v1.3 for BGM210L Modules

PCN Issue Date: Jun 26, 2024

Effective Date: Oct 02, 2024

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.4 for BGM210P and MGM210P modules (jointly referred to as xGM210P) and version 1.3 for BGM210L modules.

Links to datasheets:

BGM210P: https://www.silabs.com/documents/public/data-sheets/bgm210p-datasheet.pdf MGM210P: https://www.silabs.com/documents/public/data-sheets/mgm210p-datasheet.pdf BGM210L: https://www.silabs.com/documents/public/data-sheets/bgm210l-datasheet.pdf

Reason for Change

Common Notable changes for BGM210L v1.3, BGM210P v1.4 and MGM210P v1.4 are:

- Changed mentions of "Secure Element" to "Secure Engine".
- Updated ordering information: Changed Bluetooth version from 5.3 to 5.x and added note regarding Bluetooth 5.x support.

• Absolute Maximum Ratings: Added "DC voltage on RESETn pin" specification and added note regarding RESETn pin's pullup to VDD.

- Updated General Operating Conditions: Added External Clock Input and DPLL Reference Clock maximum specifications.
- Changed symbol SAT to RXSAT in RF Receiver Characteristics for Bluetooth Low Energy at 1 Mbps

Updated Bluetooth Qualification information.

Changes specific to BGM210L v1.3

• Updated China (SRRC), Japan (MIC) and Israel (MOC) chapters improved; Jordan, Singapore, United Arab Emirates, Saudi Arabia chapter removed

Changes Specific BGM210P v1.4 and MGM210P v1.4 are:

- Removed reference to EFR32BG21B and EFR32MG21B Data Sheet.
- Updated general Operating Conditions: Removed Min and Max from HCLK Radio frequency.
- Added footnote about external capacitance to GAIN = 2 in Low Frequency Crystal Oscillator
- GPIO Pins:
- Updated test condition for VOL and VOH from "IOVDD = 1.62 V" to "IOVDD = 1.71 V"
- Added "RESETn low time to ensure pin reset" specification.
- GPIO Alternate Function Table: Added VREFP pin

Impact on Form, Fit, Function, Quality, Reliability

None.

Product Identification

Existing Part # BGM210LA22JIF2 BGM210LA22JIF2R BGM210LA22JNF2 BGM210LA22JNF2R BGM210PA22JIA2 BGM210PA22JIA2R BGM210PA32JIA2R BGM210PB22JIA2 BGM210PB22JIA2R BGM210PB32JIA2 BGM210PB32JIA2R BGM210PBPRD1A2 BGM210PBPRD1A2R MGM210PA22JIA2 MGM210PA22JIA2R MGM210PA32JIA2 MGM210PA32JIA2R MGM210PB22JIA2 MGM210PB22JIA2R MGM210PB32JIA2 MGM210PB32JIA2R MM21PA3IP1612A2 MM21PA3IP1612A2R MM21PA3IP1613A2 MM21PA3IP1613A2R MM21PA3IPZ095A2 MM21PA3IPZ095A2R

Last Date of Unchanged Product: Oct 02, 2024

Qualification Samples

N/A

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.

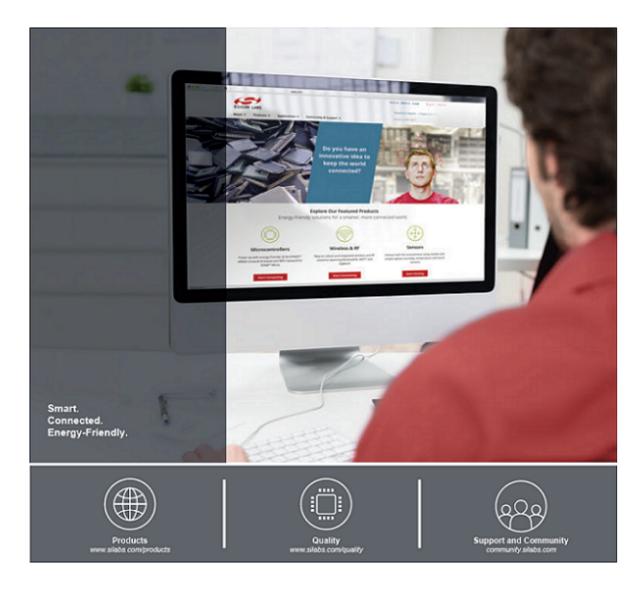
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCN@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <u>http://www.silabs.com/profile</u>

Qualification Data

N/A



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized to key set on the advector and designed or authorized to result in significant personal injury or death. Silicon Labs products are not designed or authorized to result in significant personal injury or death. Silicon Labs products are not designed or authorized to weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOmodem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc. 400 West Cesar Chavez Austin, TX 78701

http://www.silabs.com