



2409111569 Errata Update v0.3 for EFR32BG27 and EFR32MG27

Bulletin Issue Date: Sep 11, 2024

Effective Date: Sep 11, 2024

Description of Change

Silicon Labs is pleased to announce the release of errata version 0.3 for EFR32BG27 and EFR32MG27 devices.

Reason for Change

Common changes for EFR32BG27 v0.3 and EFR32MG27 v0.3 are:

- Added CUR_E305 – Increased Leakage Current When PA00 Pin Voltage Is Above GND
- Added EUSART_E303 – EUSART Receiver Enters Lockup State when Using Low Frequency IrDA Mode
- Added EUSART_E304 – Incorrect Stop Bits Lock Receiver

Changes specific to EFR32BG27 v0.3:

- Updated errata description and workaround for DCDC_E303 (DC-DC gets stuck in Boost Start-up if VBAT POR toggles after DVDD is powered up). This issue is resolved on revision B devices.

Changes specific to EFR32MG27 v0.3:

- Added DCDC_E303 (DC-DC gets stuck in Boost Start-up if VBAT POR toggles after DVDD is powered up). This issue is resolved on revision B devices.

Links to errata for full details:

EFR32BG27: <https://www.silabs.com/documents/public/errata/efr32bg27-errata.pdf>

EFR32MG27: <https://www.silabs.com/documents/public/errata/efr32mg27-errata.pdf>

Product Identification

Existing Part #

EFR32MG27C140F768IM32-B
 EFR32MG27C140F768IM32-BR
 EFR32MG27C230F768IM32-B
 EFR32MG27C230F768IM32-BR
 EFR32MG27C140F768IM40-B
 EFR32MG27C140F768IM40-BR
 EFR32MG27C230F768IM40-B
 EFR32MG27C230F768IM40-BR
 EFR32BG27C140F768IM32-B
 EFR32BG27C140F768IM32-BR
 EFR32BG27C230F768IM32-B
 EFR32BG27C230F768IM32-BR
 EFR32BG27C320F768GJ39-B
 EFR32BG27C320F768GJ39-BR
 EFR32BG27C320MZ087GJ39-B
 EFR32BG27C320MZ087GJ39-BR
 EFR32BG27C140F768IM40-B
 EFR32BG27C140F768IM40-BR
 EFR32BG27C230F768IM40-B
 EFR32BG27C230F768IM40-BR

Kit Identification

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

Review updated errata.

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Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>