



2412061605 Errata Update for SiWG917 SoC and SiWN917 NCP v0.1

Bulletin Issue Date: Dec 06, 2024

Effective Date: Dec 06, 2024

Description of Change

Silicon Labs announces the release of the following errata:

- v0.1 for SiWG917 SoC: <https://www.silabs.com/documents/public/errata/siwg917-soc-ic-errata.pdf>
- v0.1 for SiWN917 NCP: <https://www.silabs.com/documents/public/errata/siwn917-ncp-ic-errata.pdf>

Reason for Change

The following errata have been added for SiWG917 SoC:

- Added OSC_32kHz_E301 (Recommendation for an External XTAL as Mandatory Requirement)
- Added OSC_32kHz_E302 (Device Hangs on Application Reset and after FW Upgrade by Commander)
- Added OSC_32kHz_E303 (ULP MCU Calendar Peripheral is Inaccurate)

The following errata have been added for SiWN917 NCP:

- Added OSC_32kHz_E321 (Recommendation for an External XTAL as Mandatory Requirement)

Please review the errata document for detailed description, affected conditions/impacts, workaround (if available) and resolution (if available).

Product Identification

Existing Part #
SIWG917M100MGTBA
SIWG917M100MGTBAR
SIWG917M110LGTBA
SIWG917M110LGTBAR
SIWG917M111MGTBA
SIWG917M111MGTBAR
SIWG917M111XGTBA
SIWG917M111XGTBAR
SIWG917M121XGTBA
SIWG917M121XGTBAR
SIWG917M141XGTBA
SIWG917M141XGTBAR
SIWN917M100LGTBA
SIWN917M100LGTBAR

Kit Identification

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

Review errata.

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