



## EFR32FG13 Dual Band Radio Board

2.4 GHz / 915 MHz 19 dBm, VMCU to PAVDD

Board Function	Page
Title Page	1
RF, Antenna & Power	2
EFR32 Signal Assignments	3
WSTK Connectors & Board ID	4

Revision History	
Rev.	Description
A00	Initial revision.
A01	Updated EFR32 revision to Rev. C. Fixing 32 kHz crystal P/N.
A02	Updated EFR32 revision to Rev. D.

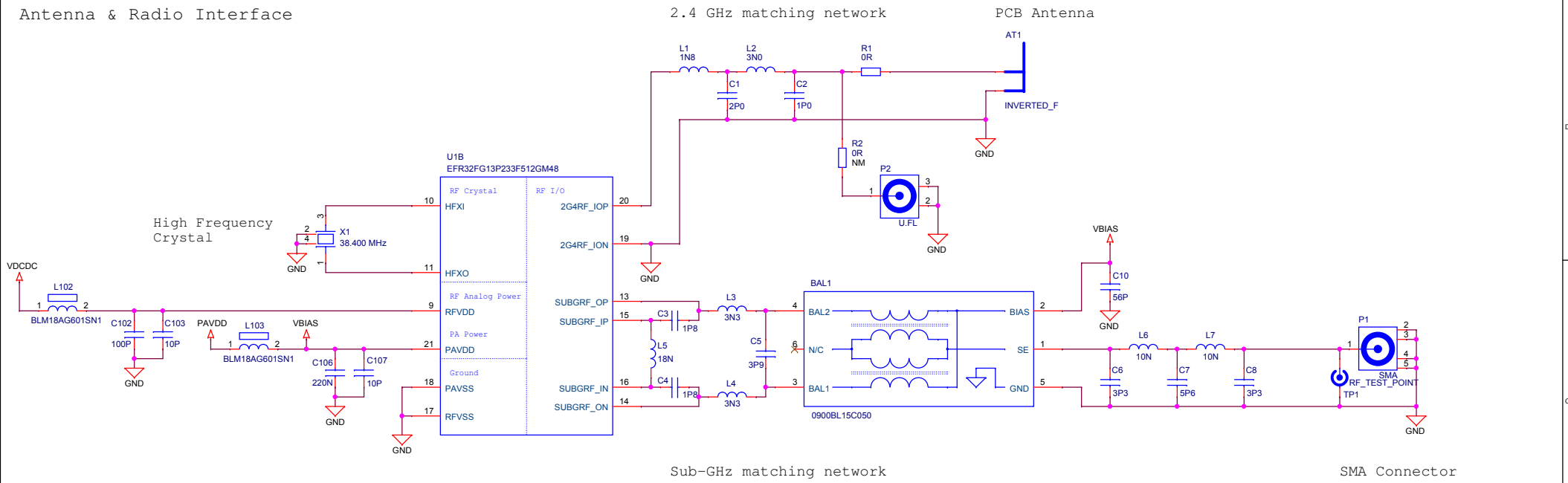
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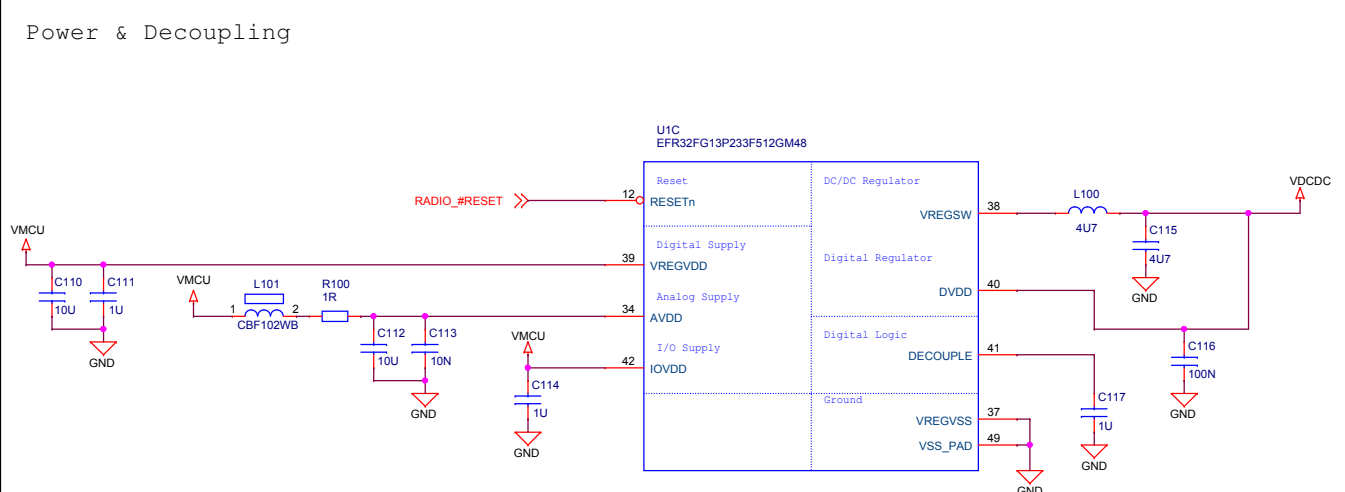
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Designed: JSH Approved: JNO		Page Title	
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Size A3	BOM Doc No: <Cage Code>	Document number <b>BRD4255A</b>	Revision A02
Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Wednesday, May 15, 2019
		Sheet 1 of 4	

Antenna & Radio Interface

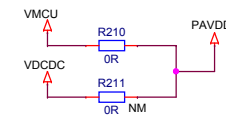


Power & Decoupling

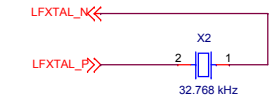


PAVDD Configuration

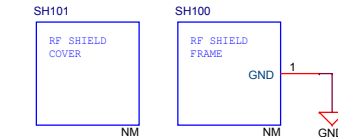
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



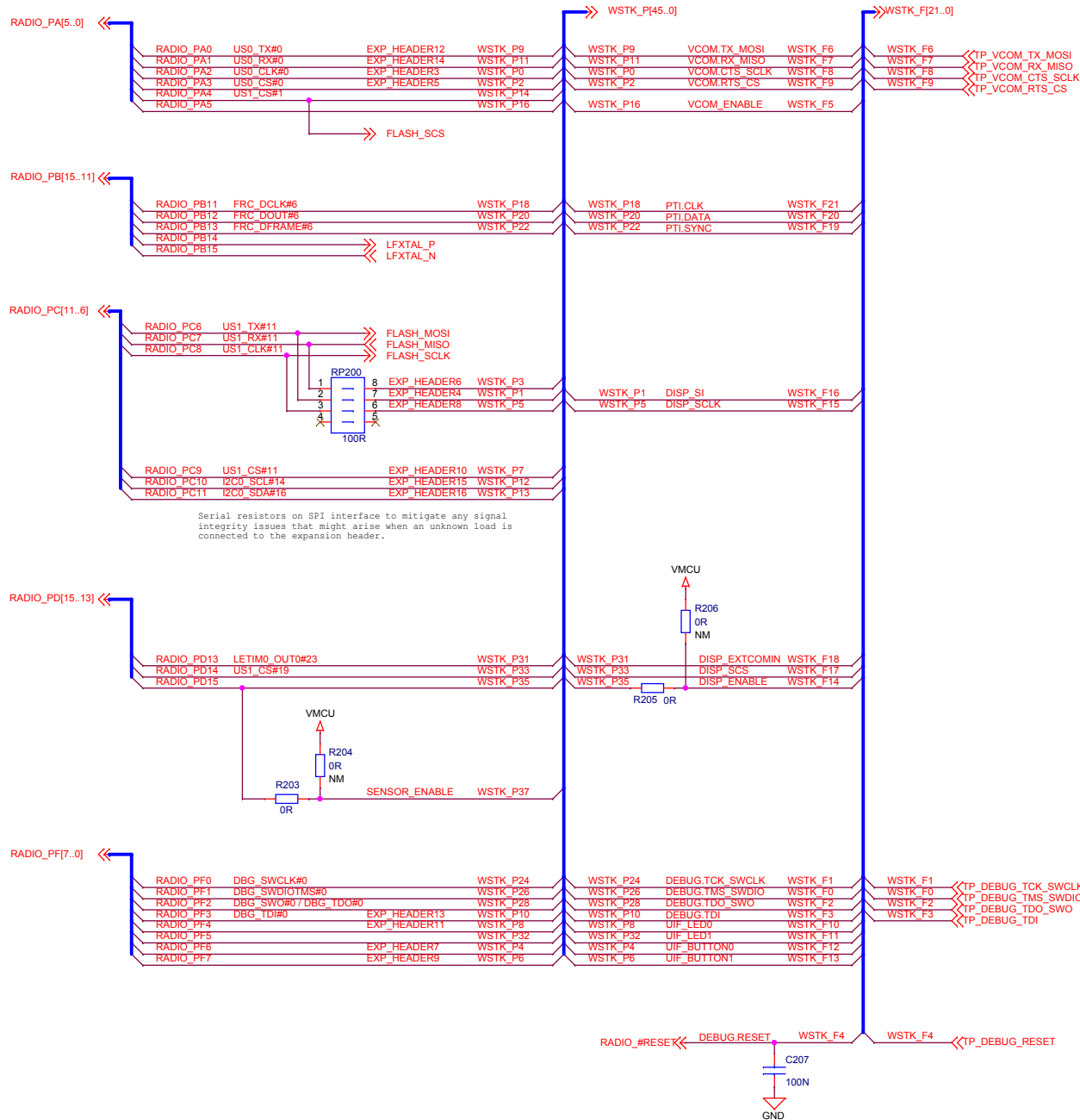
RF Shielding



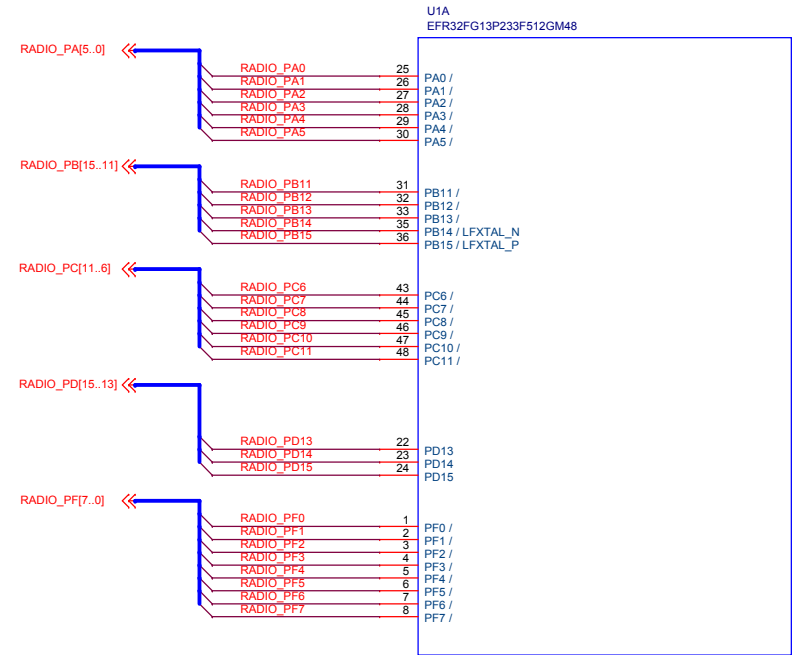
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Page Title		Revision
<b>RF, Antenna &amp; Power</b>		<b>A02</b>
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		Sheet 2 of 4

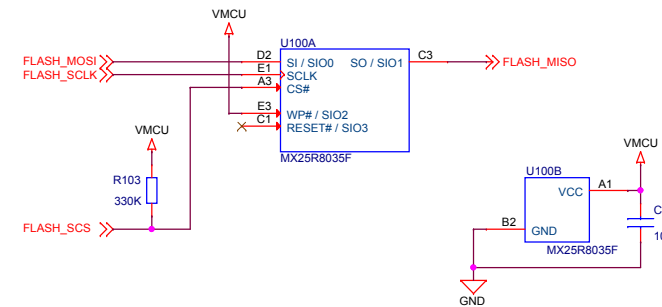
# EFR32 Pin Mapping



# EFR32 I/O Port Pins



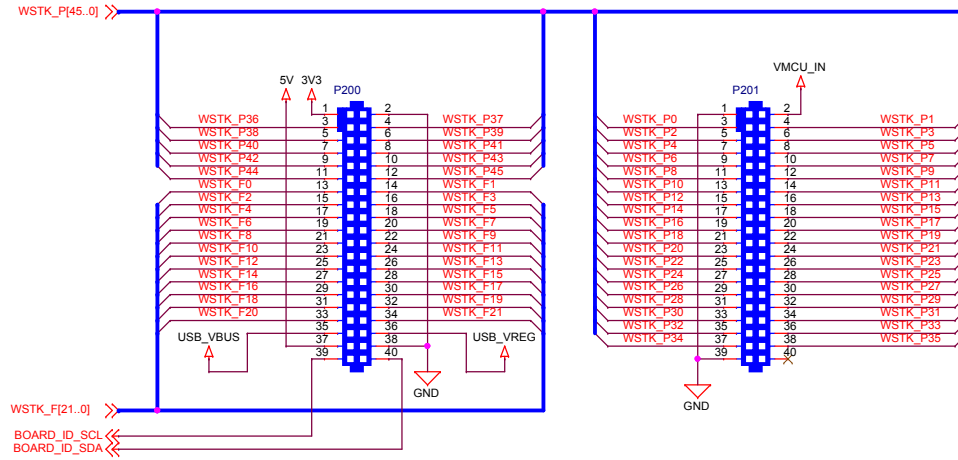
# Serial Flash



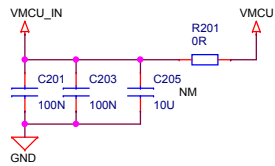
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		Revision <b>A02</b>	
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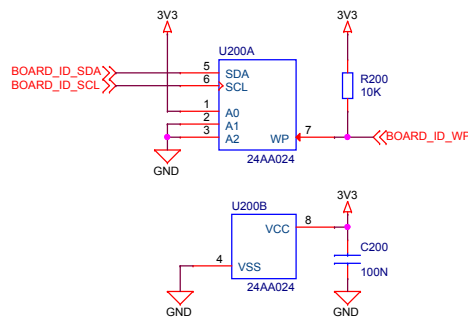
# WSTK Connectors



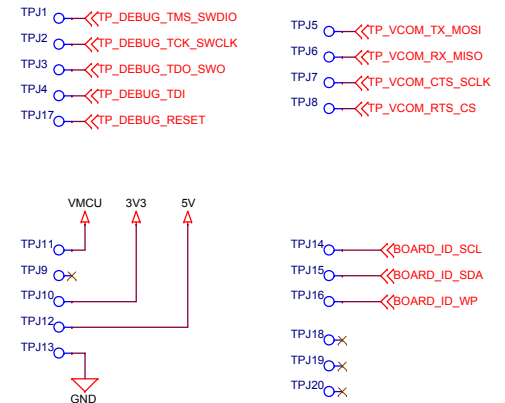
## WSTK Power Decoupling



## Board Identification



## Test Points



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		<b>WSTK Connectors &amp; Board ID</b>	
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	Sheet Modified Date: Wednesday, May 15, 2019		Sheet 4 of 4