



EFR32FG14 Dual Band Radio Board

2.4 GHz / 868 MHz 13 dBm, VDCDC to PAVDD, TCXO

Board Function	Page
Title Page	1
RF, Antenna & Power	2
EFR32 Signal Assignments	3
WSTK Connectors & Board ID	4

LEGAL NOTICE:
 SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC. IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.

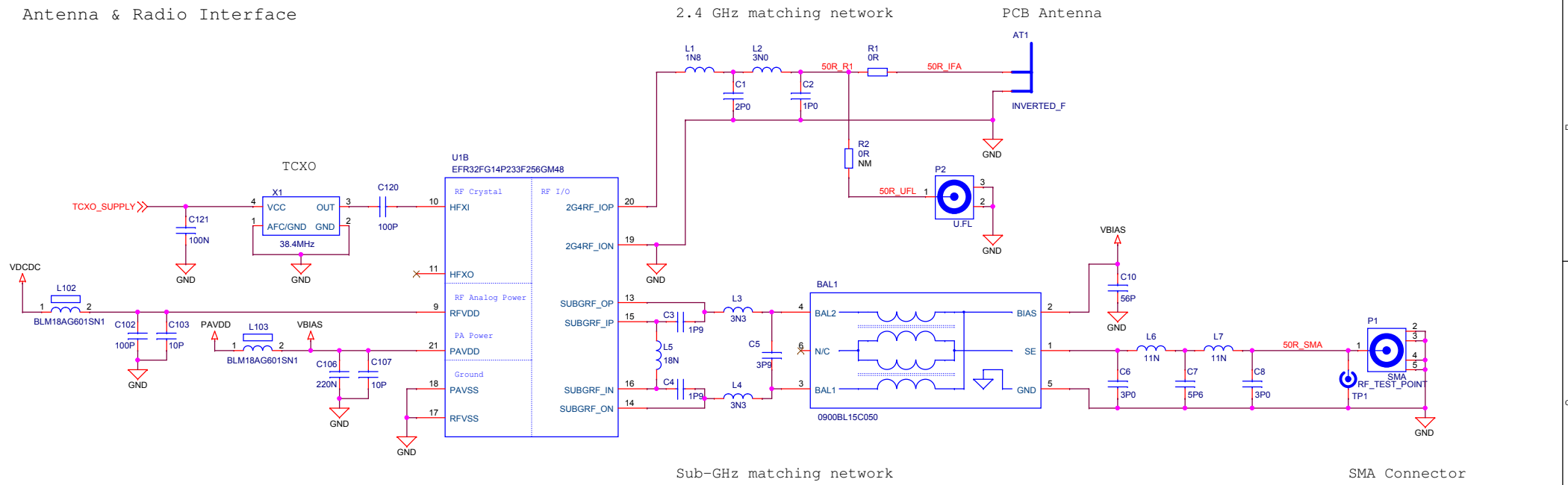


Revision History

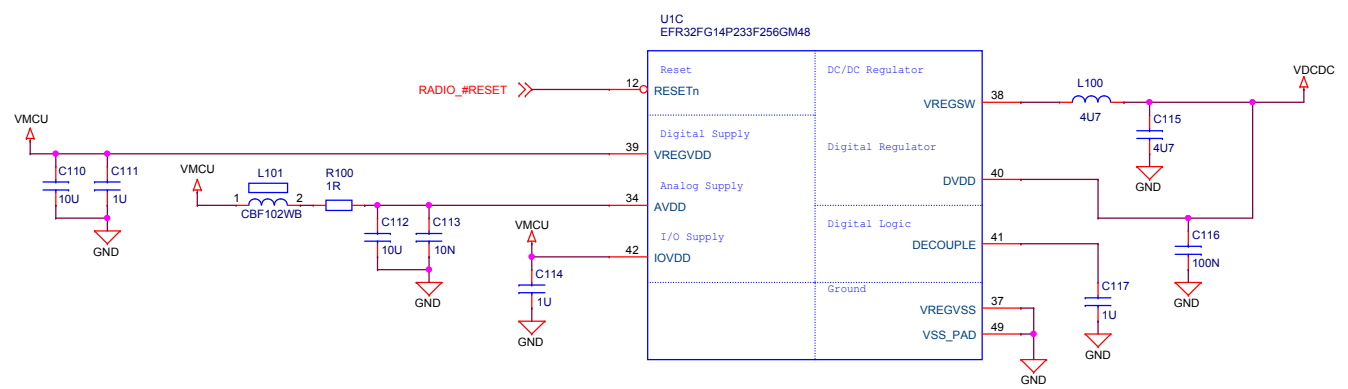
Rev.	Description
A00	Initial production revision.

		Schematic Title	
		EFR32FG14 2400/868 MHz Radio Board with TCXO	
Designed: TAB Approved: JSH		Page Title	
		Title Page	
Size A3	BOM Doc No: <Cage Code>	Document number BRD4262B	Revision A00
Design Created Date: Friday, November 03, 2017		Sheet Created Date Friday, November 03, 2017	Sheet Modified Date Monday, December 02, 2019
		Sheet 1 of 4	

Antenna & Radio Interface

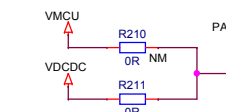


Power & Decoupling

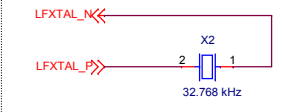


PAVDD Configuration

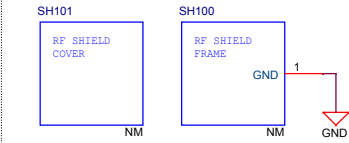
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



Schematic Title
EFR32FG14 2400/868 MHz Radio Board with TCXO

Page Title
RF, Antenna and Power

Document number
BRD4262B

Design Created Date:
Friday, November 03, 2017

Approved:
JSH

BOM Doc No:
<Cage Code>

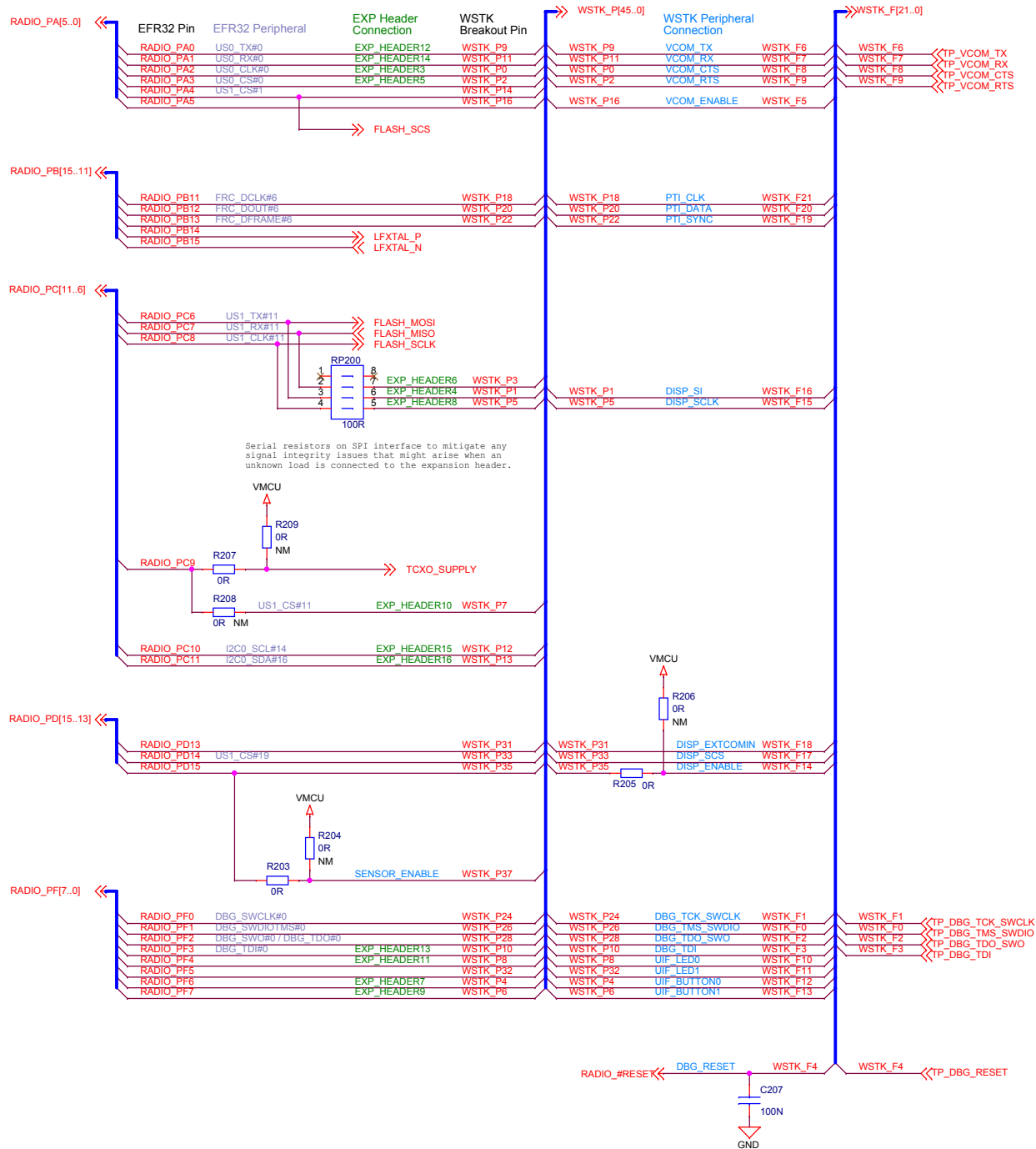
Sheet Created Date
Friday, November 03, 2017

Revision
A00

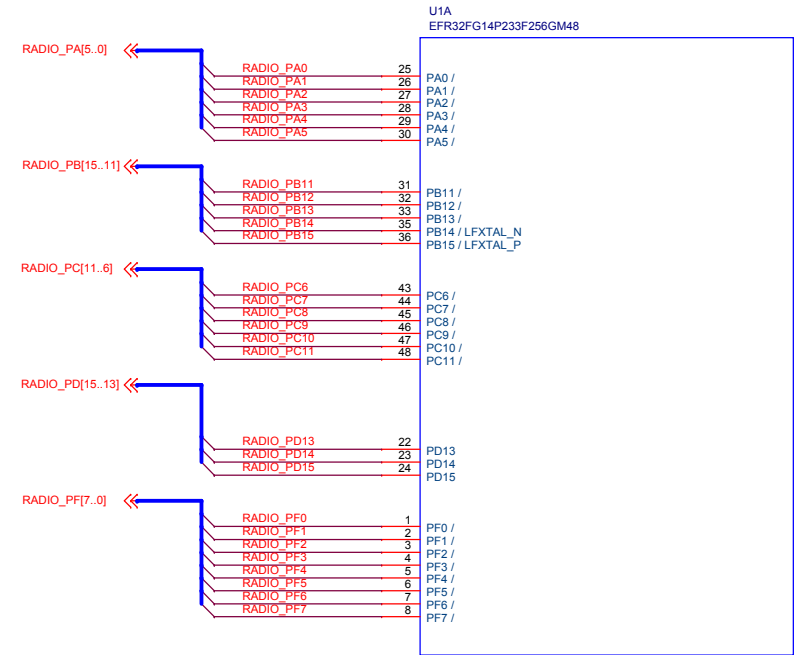
Sheet Modified Date
Monday, December 02, 2019

Sheet
2 of 4

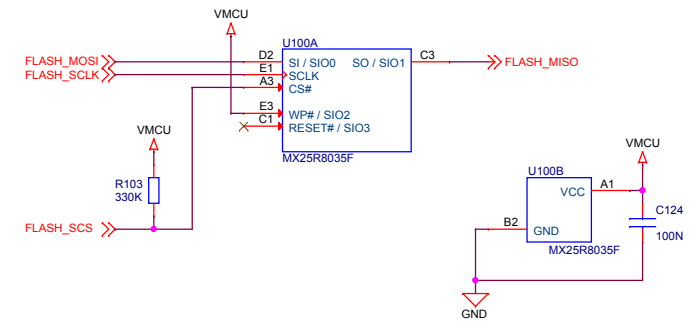
EFR32 Pin Mapping



I/O Port Pins

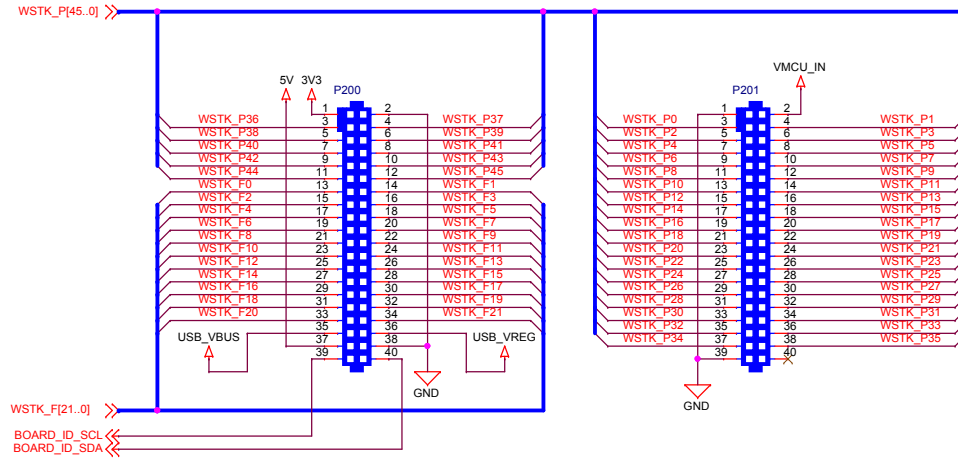


Serial Flash

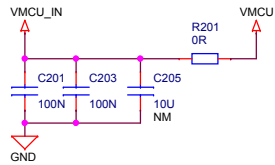


		Schematic Title	
		EFR32FG14 2400/868 MHz Radio Board with TCXO	
Designed: TAB Approved: JSH		Page Title	
		EFR32 Signal Assignments	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
Design Created Date: Friday, November 03, 2017	Sheet Created Date: Friday, November 03, 2017	Document number: BRD4262B	Revision: A00
	Sheet Modified Date: Monday, December 02, 2019		Sheet 3 of 4

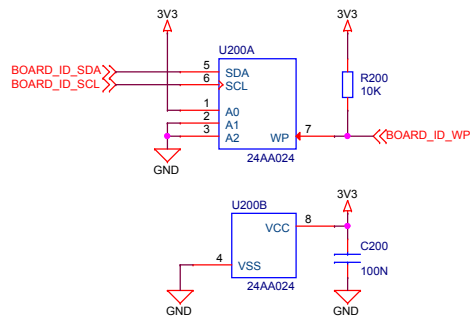
WSTK Connectors



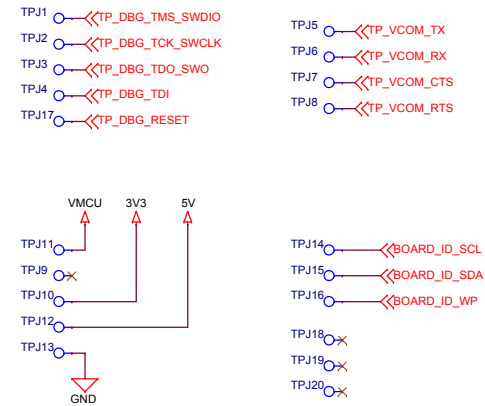
WSTK Power Decoupling



Board Identification



Test Points



		Schematic Title	
		EFR32FG14 2400/868 MHz Radio Board with TCXO	
Designed: TAB Approved: JSH		Page Title	
		WSTK Connectors & Board ID	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
Design Created Date: Friday, November 03, 2017	Sheet Created Date: Friday, November 03, 2017	BRD4262B	A00
	Sheet Modified Date: Monday, December 02, 2019		Sheet 4 of 4