



EFR32FG23B Radio Board	
434 MHz 10 dBm	
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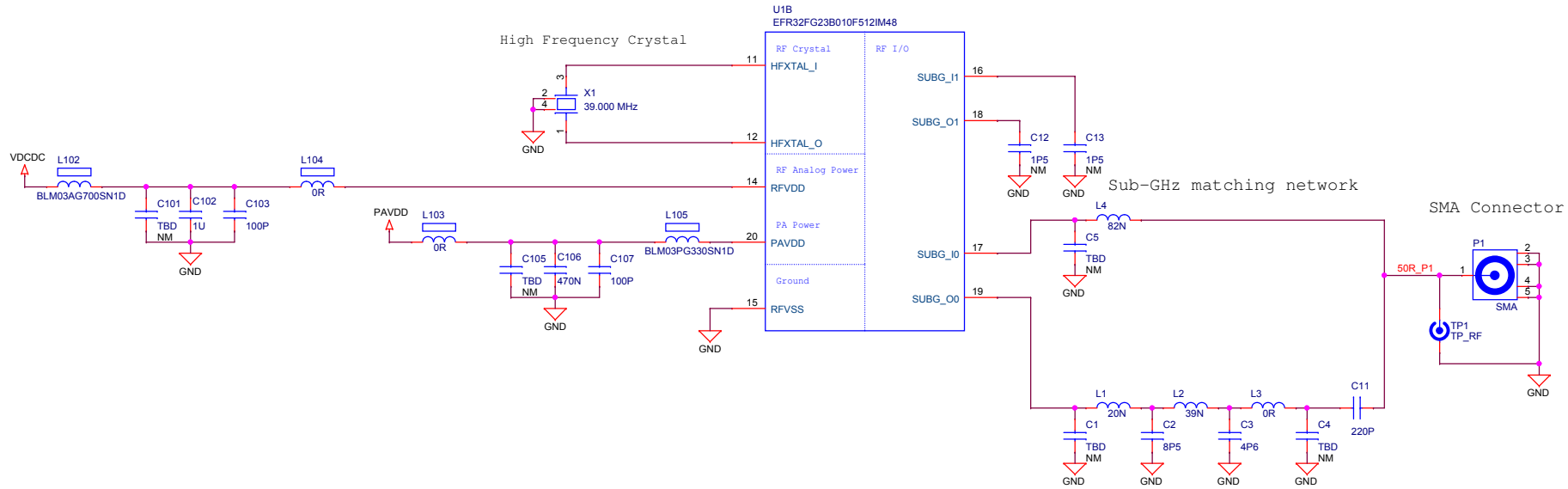


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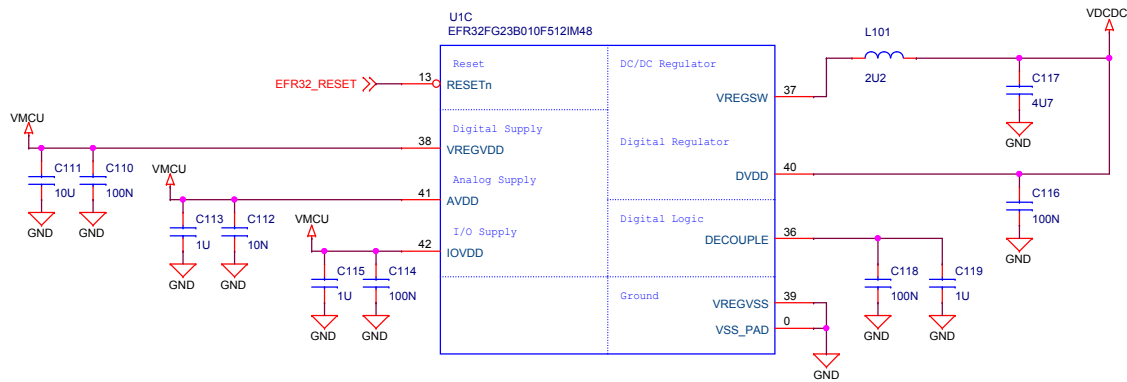
Revision History	
Rev.	Description
A00	Initial production release.

 SILICON LABS		Board Name	EFR32FG23B 434 MHz 10 dBm Radio Board
		Page Title	Title Page
Designed TAB	Approved RGU	Board Number	BRD4265B
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Radio Interface

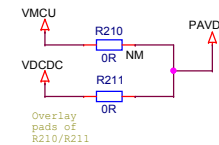


Power & Decoupling

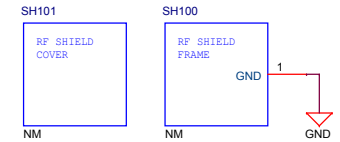


PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount

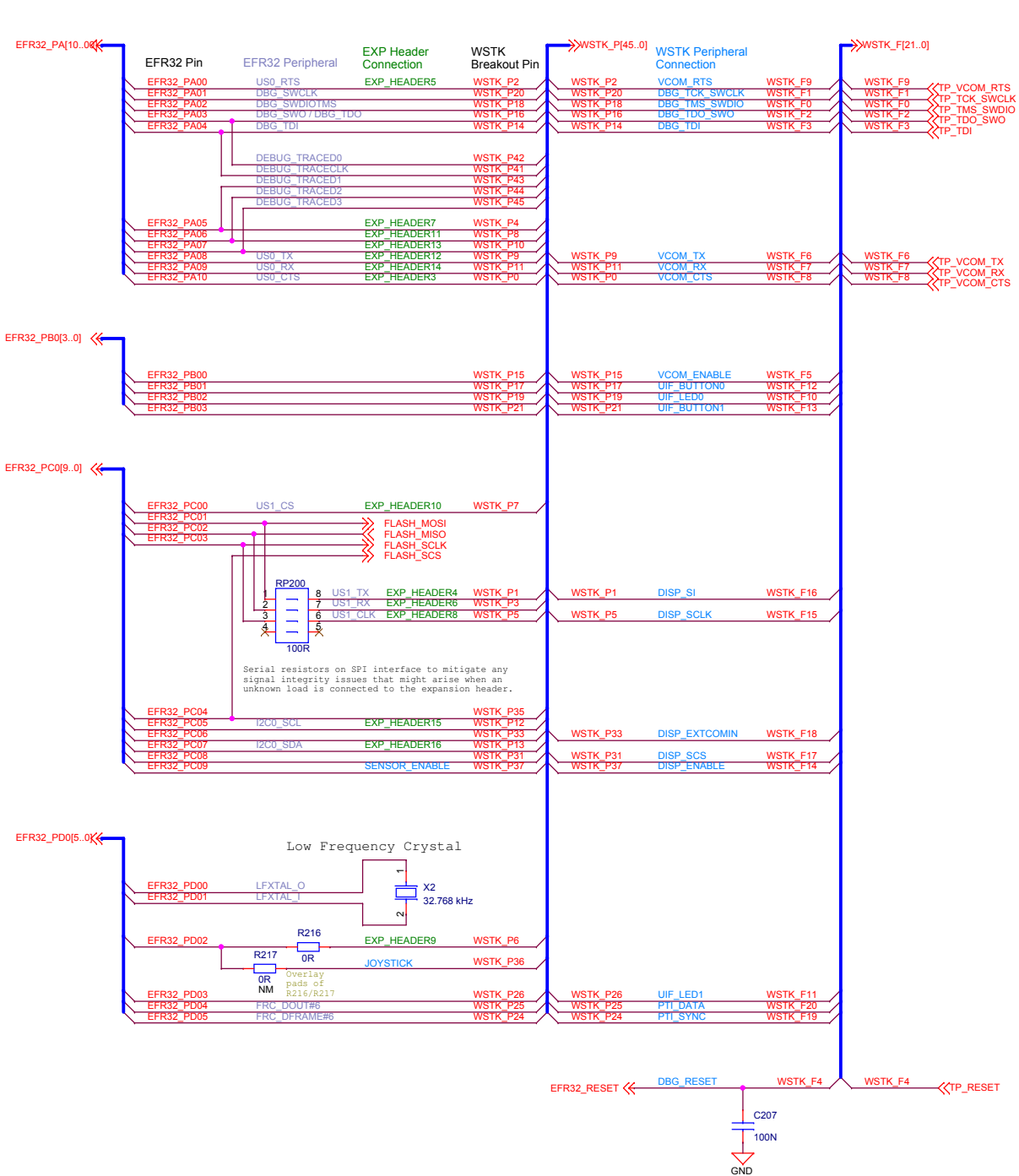


RF Shielding

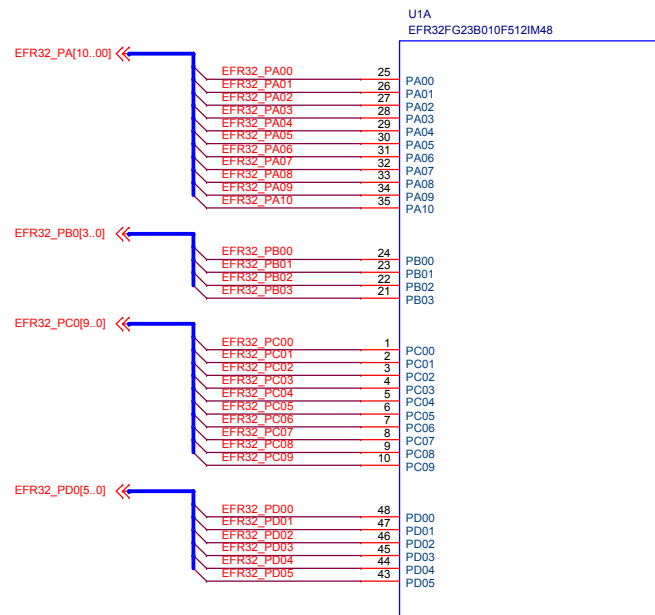


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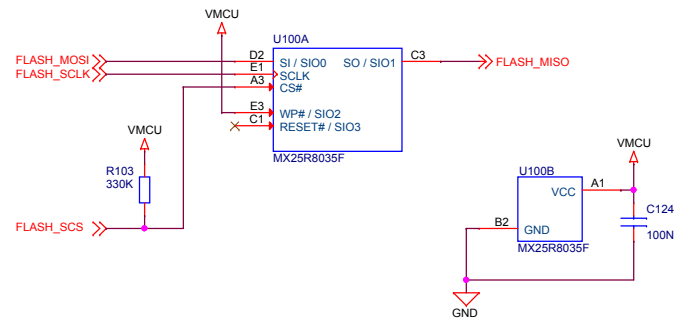
Pin Mapping



I/O Port Pins

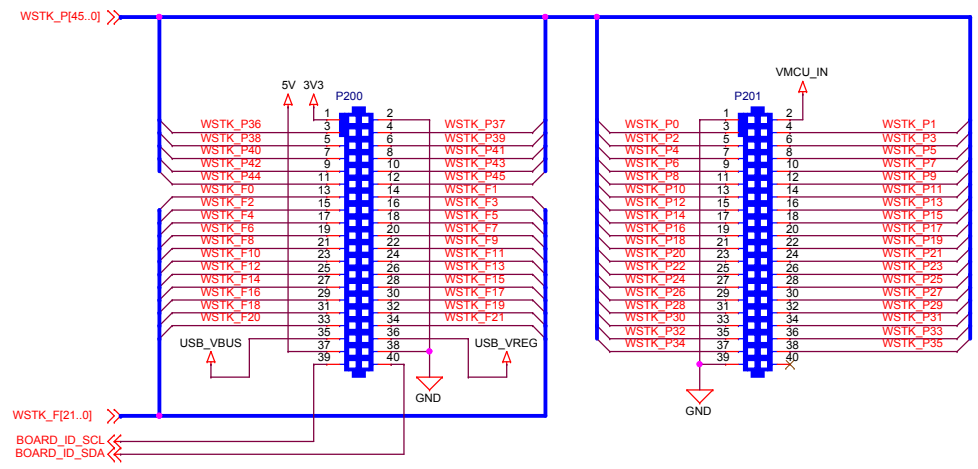


Serial Flash

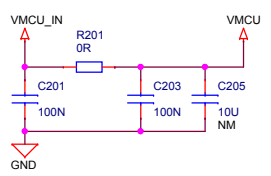


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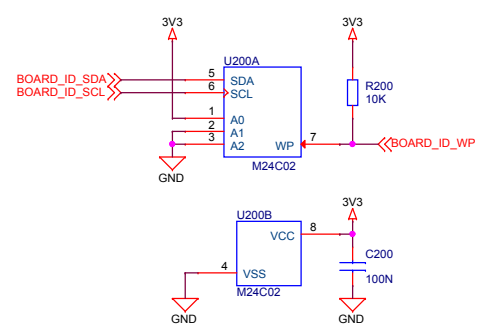
WSTK Connectors



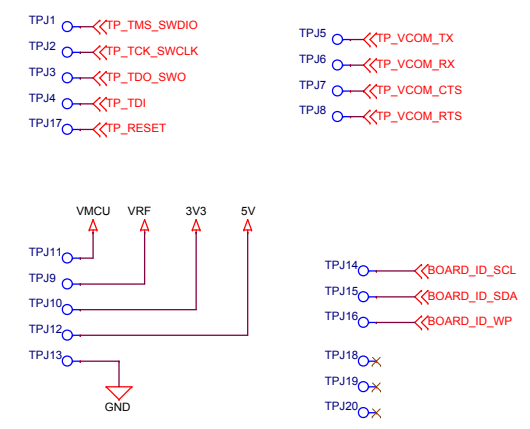
WSTK Power Decoupling



Board Identification



Test Points



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